

LCD PRODUCT SPECIFICATION

PART NUMBER:	USMPG-TQ12864B-TBYBH-P1
DESCRIPTION:	128x64 Graphic LCD; STN Blue Display Mode; Transflective, Positive
	with Yellow-Green LED Backlight and 6 O'Clock Viewing Direction.

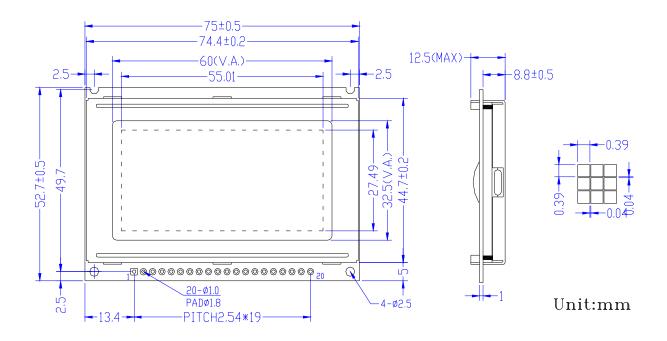
ISSUE DATE	APPROVED BY	CHECKED BY	PREPARED BY
	(Customer Use Only)		
PROPRIETARY NOTE:	THIS SPECIFICATION IS THE PROPERTY OF COPIED WITHOUT THE WRITTEN PERMINERO		AND MUST BE RETURNED TO



1.Features

- Display format: 128*64 dots matrix graphic
- STN (Yellow-Green) mode, transflective
- Easy interface with 8-bit MPU
- Low power consumption
- LED backlight (yellow-green)
- Viewing angle: 6 O'clock
- Driving method: 1/64 duty, 1/9 bias
- LCD driver IC: NT7108C NT7107C
- Connector: zebra
 Building DC/DC

2. Outline dimension

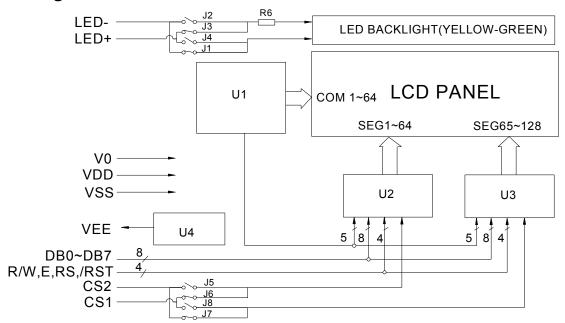


3. Absolute maximum ratings

Item	Symbol		Unit		
Power voltage	V _{DD} -V _{SS}	0	-	7.0	V
Input voltage	Vin	VSS	_	VDD	V
Operating temperature range	Vop	-20	-	+70	℃
Storage temperature range	Vst	-30	-	+80	C



4.Block diagram

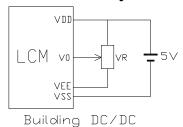


5.Interface pin description

Pin No.	Pin Name	Input/ Output	External Connection	Function
1	VDD		Davier Comple	VDD: +5.0V
2	VSS		Power Supply	VSS: GND
3	V0			Contrast adjust
4~11	DB0~DB7		MPU	Data bus. Three state I/O common terminal.
12	CS1	_	MPU	Chip selection. In order to interface data for input or output, the terminals have to be High.
13	CS2	_	MPU	Chip selection. In order to interface data for input or output, the terminals have to be High.
14	/RST		MPU	Reset signal. The LCM can be initialized by setting /RESET=L.
15	R/W		MPU	Read or Write select signal
16	D/I		MPU	Data or Instruction register select signal.
17	Е		MPU	Operation (data read/write) enable signal
18	VEE			Negative voltage output
19	LED+		Power Supply	Power supply for LED backlight (5.0V)
20	LED-		Power Supply	Power supply for LED backlight (0 V)



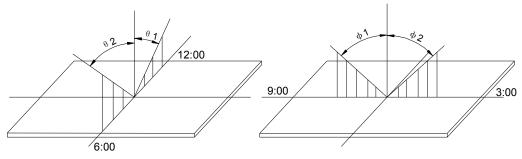
6.Contrast adjust



VDD~V0: LCD Driving voltage

VR: 10k~20k

7. Optical characteristics

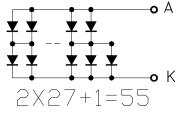


STN type display module (Ta=25°C, VDD=5.0V)

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Viewing angle	θ 1			20		
	θ 2	Cr≥3		40		dog
	Ф1	Or > 3		35		deg
	Ф2			35		
Contrast ratio	Cr		-	10	-	-
Response time (rise)	Tr	-	-	200	250	mo
Response time (fall)	Tr	-	_	300	350	ms

8. Electrical characteristics

Backlight circuit diagram(LED: 2X27+1)



LED ratings

Item	Symbol	Min	Тур.	Max	Unit
Forward Voltage	V_{F}	3.8	4.1	4.4	V
Forward current	IF	ı	275	-	mA
Power	Р			800	mW
Peak wave length	λр	570	572	575	nm
Luminance	Lv	1	270	-	Cd/m ²



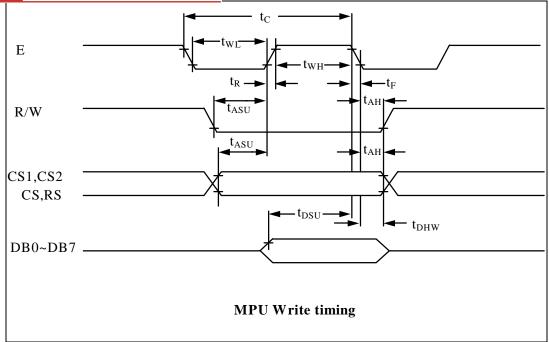
DC Electrical Characteristics

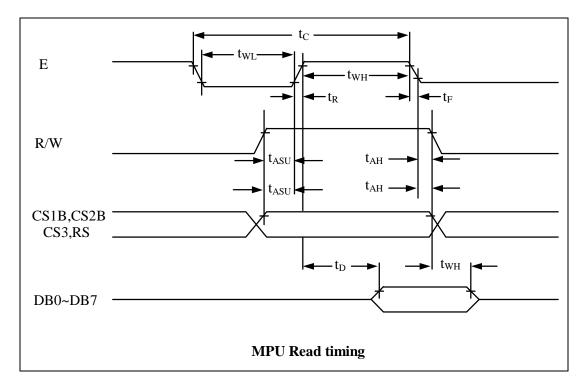
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Supply voltage for LCD	V_{DD} - V_0	Ta =25℃	-	9.5	-	V
Input voltage	$V_{ extsf{DD}}$		4.7	5.0	5.5	
Supply current	lod	Ta=25°C, V _{DD} =5.0V	-	10	15	mA
Input leakage current	I LKG		-	-	5.0	uA
"H" level input voltage	Vн		2.2	-	V _{DD}	
"L" level input voltage	VL	Twice initial value or less	0	-	0.6	V
"H" level output voltage	Voh	LOH=-0.25mA	2.4	-	-	
"L" level output voltage	Vol	LOH=1.6mA	-	-	0.4	
Backlight supply current	F	VDD=5.0V,R=6.8 Ω	-	275	-	mA

MPU Interface

Characteristic	Symbol	Min	Тур	Max	Unit
E Cycle	t _C	1000	-	-	
E High Level Width	t _{WH}	450	-	-	
E Low Level Width	t _{WL}	450	-	-	
E Rise Time	t _R	-	-	25	ns
E Fall Time	t _F	-	-	25	
Address Set-Up Time	t _{ASU}	140	-	-	
Address Hold Time	t _{AH}	10	-	-	1
Data Set-Up Time	t _{su}	200	-	-	
Data Delay Time	t _D	-	-	320	
Data Hold Time (Write)	t _{DHW}	10	-	-	1
Data Hold Time (Read)	t _{DHR}	20	-	-	1







9. OPERATING PRINCIPLES & METHODS

Ø I/O Buffer

Input buffer controls the status between the enable and disable of chip. Unless the CS1B to CS3 is in active mode, Input or output of data and instruction does not execute. Therefore internal state is not change. But RSTB and ADC can operate regardless CS!B-CS3.

Ø Input register



Input register is provided to interface with MPU which is different operating frequency. Input register stores the data temporarily before writing it into display RAM.

When CS1B to CS3 are in the active mode, R/W and RS select the input register. The data from MPU is written into input register. Then writing it into display RAM. Data latched for falling of the E signal and write automatically into the display data RAM by internal operation.

Ø Output register

Output register stores the data temporarily from display data RAM when CS1B, CS2B and CS3 are in active mode and R/W and RS=H, stored data in display data RAM is latched in output register. When CS1B to CS3 is in active mode and R/W=H, RS=L, status data (busy check) can read out.

To read the contents of display data RAM, twice access of read instruction is needed. In first access, data in display data RAM is latched into output register. In second access, MPU can read data which is latched. That is to read the data in display data RAM, it needs dummy read. But status read is not needed dummy read.

RS	R/W	Function						
	L	Instruction						
	H Status read (busy check)							
Н	L	Data write (from input register to display data RAM)						
''	Н	Data read (from display data RAM to output register)						

Ø Reset

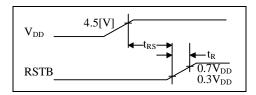
The system can be initialized by setting RSTB terminal at low level when turning power on, receiving instruction from MPU. When RSTB becomes low, following procedure is occurred.

- 1. Display off
- 2. Display start line register become set by 0.(Z-address 0)

While RSTB is low, No instruction except status read can by accepted. Therefore, execute other instructions after making sure that DB4= (clear RSTB) and DB7=0 (ready) by status read instruction. The conditions of power supply at initial power up are shown in table 1.

Table 1. Power Supply Initial Conditions

Item	Symbol	Min	Тур	Max	Unit
Reset Time	t _{RS}	1.0	-	-	us
Rise Time	t_R	-	-	200	ns



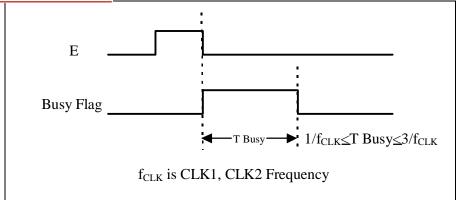
Ø Busy flag

Busy flag indicates that NT7108CB is operating or no operating. When busy flag is high, NT7108CB is in internal operating .

When busy flag is low, NT7108CB can accept the data or instruction.

DB7indicates busy flag of the NT7108CB.





Ø Display On/Off Flip-Flop

The display on/off flip-flop makes on/off the liquid crystal display. When flip-flop is reset (logical low), selective voltage or non selective voltage appears on segment output terminals. When flip-flop is set (logic high), non selective voltage appears on segment output terminals regardless of display RAM data. The display on/off flip-flop can changes status by instruction. The display data at all segment disappear while RSTB is low.

The status of the flip-flop is output to DB5 by status read instruction.

The display on/off flip-flop synchronized by CL signal.

Ø X Page Register

X page register designates pages of the internal display data RAM. Count function is not available. An address is set by instruction.

Ø Y address counter

Y address counter designates address of the internal display data RAM. An address is set by instruction and is increased by 1 automatically by read or write operations of display data.

Ø Display Data RAM

Display data RAM stores a display data for liquid crystal display. To indicate on state dot matrix of liquid crystal display, write datra1. The other way, off state, writes 0.

Display data RAM address and segment output can be controlled by ADC signal.

ADC=H => Y-address 0: S1~Y address 63: S64

ADC=L => Y-address 0: S64~Yaddress 63: S1

ADC terminal connect the V_{DD} or V_{SS} .

Ø Display Start Line Register

The display start line register indicates of display data RAM to display top line of liquid crystal display. Bit data (DB<0.5>) of the display start line set instruction is latched in display start line register. Latched data is transferred to the Z address counter while FRM is high, presetting the Z address counter. It is used for scrolling of the liquid crystal display screen.



10.Display Control Instruction

The display control instructions control the internal state of the NT7108CB. Instruction is received from MPU to NT7108CB for the display control. The following table shows various instructions.

Instruction	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function
Read Display Date	1	1	Read data							Reads data (DB[7:0]) from display data RAM to the data bus.	
Write Display Date	1	0	Write data							Writes data (DB[7:0]) into the DDRAM. After writing instruction, Y address is incriminated by 1 automatically	
Status Read	0	1	Bus y	0	ON/ OFF	Re- set	0	0	0	0	Reads the internal status BUSY 0: Ready 1: In operation ON/OFF 0: Display ON 1: Display OFF RESET 0: Normal 1: Reset
Set Address (Y address)	0	0	0	1		Υ	addres	ss (0~63	3)	•	Sets the Y address at the column address counter
Set Display Start Line	0	0	1	1		Disp	lay star	t line (0	~63)		Indicates the Display Data RAM displayed at the top of the screen.
Set Address (X address)	0	0	1	0	1	1 1 1 Page (0~7)				Sets the X address at the X address register.	
Display On/off	0	0	0	0	1	1	1	1	1	0/1	Controls the display ON or OFF. The internal status and the DDRAM data is not affected. 0: OFF, 1: ON

Ø Display On/Off

The display data appears when D is 1 and disappears when D is 0.

Though the data is not on the screen with D=0, it remains in the display data RAM.

Therefore, you can make it appear by changing D=0 into D=1.

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	1	1	1	1	1	D

Ø Set Address (Y Address)

Y address (AC0~AC5) of the display data RAM is set in the Y address counter.

An address is set by instruction and increased by 1 automatically by read or write operations of display data.

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0



Ø Set Page (X Address)

X address (AC0~AC2) of the display data RAM is set in the X address register. Writing or reading to or from MPU is executed in this specified page until the next page is set.

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	0	1	1	1	AC2	AC1	AC0

Ø Display Start Line (Z Address)

Z address (AC0~AC5) of the display data RAM is set in the display start line register and displayed at the top of the screen.

When the display duty cycle is 1/64 or others (1/32~1/64), the data of total line number of LCD screen, from the line specified by display start line instruction, is displayed.

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	1	AC5	AC4	AC3	AC2	AC1	AC0

Ø Status Read

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	0	BUS Y	0	ON/OFF	RESET	0	0	0	0

I BUSY

When BUSY is 1, the Chip is executing internal operation and no instructions are accepted. When BUSY is 0, the Chip is ready to accept any instructions.

I ON/OFF

When ON/OFF is 1, the display is on. When ON/OFF is 0, the display is off.

I RESET

When RESET is 1, the system is being initialized.

In this condition, no instructions except status read can be accepted.

When RESET is 0, initializing has finished and the system is in the usual operation condition.

Ø Write Display Data

Writes data (D0~D7) into the display data RAM.

After writing instruction, Y address is increased by 1 automatically.

Ī	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
Ī	1	0	D7	D6	D5	D4	D3	D2	D1	D0

Ø Read Display Data

Reads data (D0~D7) from the display data RAM.

After reading instruction, Y address is increased by 1 automatically.

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	1	D7	D6	D5	D4	D3	D2	D1	D0

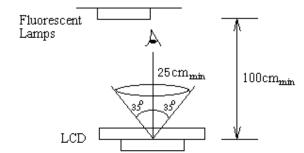


11.QUALITY SPECIFICATIONS

11.1 Standard of the product appearance test

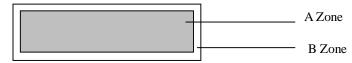
Manner of appearance test: The inspection should be performed in using $20W \times 2$ fluorescent lamps. Distance between LCM and fluorescent lamps should be 100 cm or more. Distance between LCM and inspector eyes should be 25 cm or more.

Viewing direction for inspection is 35° from vertical against LCM.



Definition of zone:

LCM



A Zone: Active display area (minimum viewing area). B Zone: Non-active display area (outside viewing area).

11.2 Specification of quality assurance

AQL inspection standard

Sampling method: GB2828-87, Level II, single sampling

Defect classification (Note: * is not including)

ect classificati	on (Note: * is not	. including)		
Classify		Item	Note	AQL
Major	Display state	Short or open circuit	1	0.65
		LC leakage		
		Flickering		
		No display		
		Wrong viewing direction		
		Contrast defect (dim, ghost)	2	
		Back-light	1,8	
	Non-display	Flat cable or pin reverse	10	
		Wrong or missing component	11	
Minor	Display	Background color deviation	2	1.0
	state	Black spot and dust	3	
		Line defect, Scratch	4	
		Rainbow	5	
		Chip	6	
		Pin hole	7	
		Protruded	12	
	Polarizer	Bubble and foreign material	3	
	Soldering	Poor connection	9	
	Wire	Poor connection	10	
	TAB	Position, Bonding strength	13	



No.	Item	Criterion
1	Short or open circuit	Not allow
	LC leakage	
	Flickering	
	No display	
	Wrong viewing direction	
	Wrong Back-light	
2	Contrast defect	Refer to approval sample
	Backgroundcolor deviation	
3	Point defect, Black spot, dust (including Polarizer) $\phi = (X+Y)/2$	Point Acceptable Qty. Size $\phi \leq 0.10$ Disregard $0.10 < \phi \leq 0.15$ 2 $0.15 < \phi \leq 0.25$ 1 $\phi > 0.25$ 0
		Unit: Inch ²
4	Line defect, Scratch	$\begin{array}{c cccc} & & & & & & \\ & & & & & \\ & & & & \\ L & & & &$
5	Rainbow	Unit: mm Not more than two color changes across the viewing area.



No	Item	Criterion
6	Chip Remark: X: Length direction Y: Short direction	Acceptable criterion $ \begin{array}{ c c c c c c c c c c c c c c c c c c c$
	Z: Thickness direction t: Glass thickness W: Terminal Width L:Glass length	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$
	icigai	Acceptable criterion $\begin{array}{ c c c c c c c }\hline X & Y & Z\\ \leqslant 3 & \leqslant 2 & \leqslant t\\ \hline \text{shall not reach to ITO} \\ \hline \end{array}$
		$\begin{array}{c ccccccccccccccccccccccccccccccccccc$
		$\begin{array}{c ccccccccccccccccccccccccccccccccccc$



No.	Item	Criterion		
7	Segment pattern W = Segment width φ = (X+Y)/2	(1) Pin hole $\phi < 0.10 \text{mm is acceptable.}$ X Y		
8	Back-light	(1) The color of backlight should correspond its specification.(2) Not allow flickering		
9	Soldering	(1) Not allow heavy dirty and solder ball on PCB. (The size of dirty refer to point and dust defect) (2) Over 50% of lead should be soldered on Land. Lead Land 50% lead		
10	Wire	 (1) Copper wire should not be rusted (2) Not allow crack on copper wire connection. (3) Not allow reversing the position of the flat cable. (4) Not allow exposed copper wire inside the flat cable. 		
11*	PCB	(1) Not allow screw rust or damage. (2) Not allow missing or wrong putting of component.		



No	Item	Criterion
12	Protruded W: Terminal Width	Acceptable criteria: $Y \le 0.4$
13	TAB	1. Position $\begin{array}{cccccccccccccccccccccccccccccccccccc$
		P (=F/TAB bonding width) ≥650gf/cm ,(speed rate: 1mm/min) 5pcs per SOA (shipment)
14	Total no. of acceptable Defect	A. Zone Maximum 2 minor non-conformities per one unit. Defect distance: each point to be separated over 10mm B. Zone It is acceptable when it is no trouble for quality and assembly in customer's end product.



11.3 Reliability of LCM

Reliability test condition:

Condition	Time (hrs)	Assessment
80°C	48	
70°C	48	No abnormalities
-30°C	48	in functions
-20°C	48	and appearance
40°C/ 90%RH	48	
0° C ← 25° C \rightarrow 50 $^{\circ}$ C (30 min ← 5 min \rightarrow 30min)	10cycles	
	80°C 70°C -30°C -20°C 40°C/ 90%RH 0°C ← 25°C →50°C	80°C 48 70°C 48 -30°C 48 -20°C 48 40°C/90%RH 48 0°C ← 25°C →50°C 10cycles

Recovery time should be 24 hours minimum. Moreover, functions, performance and appearance shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature (20±8°C), normal humidity (below 65% RH), and in the area not exposed to direct sun light.

11.4 Precaution for using LCD/LCM

LCD/LCM is assembled and adjusted with a high degree of precision. Do not attempt to make any alteration or modification. The followings should be noted.

General Precautions:

- LCD panel is made of glass. Avoid excessive mechanical shock or applying strong pressure onto the surface of display area.
- 2. The polarizer used on the display surface is easily scratched and damaged. Extreme care should be taken when handling. To clean dust or dirt off the display surface, wipe gently with cotton, or other soft material soaked with isoproply alcohol, ethyl alcohol or trichlorotriflorothane, do not use water, ketone or aromatics and never scrub hard.
- 3. Do not tamper in any way with the tabs on the metal frame.
- 4. Do not make any modification on the PCB without consulting USMP
- 5. When mounting a LCM, make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
- 6. Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels and also cause rainbow on the display.
- Be careful not to touch or swallow liquid crystal that might leak from a damaged cell. Any liquid crystal spreads to skin or clothes, wash it off immediately with soap and water.

Static Electricity Precautions:

- CMOS-LSI is used for the module circuit; therefore operators should be grounded whenever he/she comes into
 contact with the module.
- Do not touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.
- 3. Do not touch the connection terminals of the display with bare hand; it will cause disconnection or defective



insulation of terminals.

- 4. The modules should be kept in anti-static bags or other containers resistant to static for storage.
- 5. Only properly grounded soldering irons should be used.
- 6. If an electric screwdriver is used, it should be grounded and shielded to prevent sparks.
- 7. The normal static prevention measures should be observed for work clothes and working benches.
- Since dry air is inductive to static, a relative humidity of 50-60% is recommended.

Soldering Precautions:

- 1. Soldering should be performed only on the I/O terminals.
- 2. Use soldering irons with proper grounding and no leakage.
- 3. Soldering temperature: 280°C±10°C
- 4. Soldering time: 3 to 4 second.
- 5. Use eutectic solder with resin flux filling.
- 6. If flux is used, the LCD surface should be protected to avoid spattering flux.
- 7. Flux residue should be removed.

Operation Precautions:

- 1. The viewing angle can be adjusted by varying the LCD driving voltage Vo.
- Since applied DC voltage causes electro-chemical reactions, which deteriorate the display, the applied pulse
 waveform should be a symmetric waveform such that no DC component remains. Be sure to use the specified
 operating voltage.
- 3. Driving voltage should be kept within specified range; excess voltage will shorten display life.
- 4. Response time increases with decrease in temperature.
- 5. Display color may be affected at temperatures above its operational range.
- Keep the temperature within the specified range usage and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel-off or generate bubbles.
- 7. For long-term storage over 40°C is required, the relative humidity should be kept below 60%, and avoid direct sunlight.

Los Angeles + Austin + New York + Orlando + Shenzhen

(800) 741-7755

www.usmicroproducts.com

Displays

US Micro Products is an industrial distributor specializing in engineered display solutions. We dedicate ourselves to providing the best in displays for the medical, industrial, gaming, automotive, aerospace, military, and consumer markets.

OLEDs



TFT Displays



Open Frame Monitors



Passive LCDs



Multitouch



As a customer, you benefit from our expert knowledge, support and service which allow quick selection and design-in of the best display for your application. On hand stock and demo boards facilitate quick access and evaluation to get you going fast. Our technical sales staff and experienced design engineers provide answers to your questions as well as engineered solutions to solve your display needs.

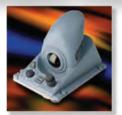
Peripheral Devices

Our full line of peripheral devices includes keyboards, trackballs, and printers. These rugged industrial products are designed to meet your demanding requirements and are available as both standard and custom solutions.

Keyboards



Trackballs Aerospace Trackballs



Joysticks



Printers

