

TFT SPECIFICATION

Part Number	USMP-T070-080048NDN-A0
Size	7"
Resolution	800 x 480
Brightness	400 cd/m²
Contrast	500:1
Viewing Angle	80/80/60/80
Operating Temp.	-30 ~ 85°C

FOR ADDITIONAL INFORMATION PLEASE CONTACT:

engineering@usmicroproducts.com

Issue Date	Approved by (customer use)	Checked by	Prepared by

PROPRIETARY NOTE: THIS SPECIFICATION IS THE PROPERTY OF US MICRO PRODUCTS AND SHALL NOT BE REPRODUCED OR COPIED WITHOUT THE WRITTEN PERMISSION OF US MICRO PRODUCTS AND MUST BE RETURNED TO US MICRO PRODUCTS UPON ITS REQUEST.

(800) 741-7755 www.usmicroproducts.com



Contents

1. Operating Precautions		4
2. General Description		5
2.1 Display Characteristics		5
2.2 Optical Characteristics		6
3. Functional Block Diagram		8
4. Absolute Maximum Ratings		9
4.1 Absolute Ratings of TFT LCD Modul	le	9
4.2 Absolute Ratings of Environment		9
5. Electrical Characteristics		10
5.1 TFT LCD Module		10
5.2 Backlight Unit		12
6. Signal Characteristic		13
6.1 Pixel Format Image		13
6.2 Signal Description		14
6.3 The Input Data Format		15
6.4 Interface Timing		16
6.5 Power ON/OFF Sequence		17
7. Connector & Pin Assignment		18
7.1 TFT LCD Signal (CN1): LVDS Conn	ector	18
7.2 LED Backlight Unit (CN2): Backlight	t Connector	18
8. Reliability Test Criteria		
9. Mechanical Characteristics		20
9.1 LCM Front View		20
9.2 LCM Rear View		21
10. Label and Packaging		22
10.1 Shipping Label (on the rear side of	f TFT-LCD display)	22
10.2 Carton Package		
11 Safety		22
11.1 Sharp Edge Requirements		22
11.2 Materials		22
11.3 Capacitors		22
11 4 National Test Lab Requirement		22



Record of Revision

Version and Date	Page	Old description	New Description
0.0 2008/1/3	All	First Edition	



1. Operating Precautions

- 1) Since front polarizer is easily damaged, please be cautious not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or soft cloth.
- 5) Since the panel is made of glass, it may be broken or cracked if dropped or bumped on hard surface.
- 6) To avoid ESD (Electro Static Discharde) damage, be sure to ground yourself before handling TFT-LCD Module.
- 7) Do not open nor modify the module assembly.
- 8) Do not press the reflector sheet at the back of the module to any direction.
- 9) In case if a module has to be put back into the packing container slot after it was taken out from the container, do not press the center of the LED Reflector edge. Instead, press at the far ends of the LED Reflector edge softly. Otherwise the TFT Module may be damaged.
- 10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11)After installation of the TFT Module into an enclosure (Notebook PC Bezel, for example), do not twist nor bend the TFT Module even momentary. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.
- 12) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.
- 13) Severe temperature condition may result in different luminance, response time.
- 14) Continuous operating TFT-LCD Module under high temperature environment may accelerate LED light bar exhaustion and reduce luminance dramatically.
- 15) The data on this specification sheet is applicable when LCD module is placed in landscape position.
- 16) Continuous displaying fixed pattern may induce image sticking. It is recommended to use screen saver or shuffle content periodically if fixed pattern is displayed on the screen.



2. General Description

This specification applies to the 7.0 inch color TFT LCD module.

Designed with wide viewing angle; wide operatiing temperature and long life LEDs backlight is well suited to be the display units for Industrial Applications.

LED driving board for backlight unit is included in this panel and the structure of the LED units is replaceable.

Built in timing controller and LVDS interface.

The screen format is intended to support the WVGA (800(H) x 480(V)) screen and 16.2M (RGB 8-bits) or 262k colors (RGB 6-bits).

A RoHS product.

2.1 Display Characteristics

The following items are characteristics summary on the table under 25 $^{\circ}\mathrm{C}$ condition:

Items	Unit	Specifications
Screen Diagonal	[inch]	7.0 (213.4mm)
Active Area	[mm]	152.40(H) x 91.44(V)
Pixels H x V		800x3(RGB) x 480
Pixel Pitch	[mm]	0.1905x 0.1905
Pixel Arrangement		R.G.B. Vertical Stripe
Display Mode		TN, Normally White
Nominal Input Voltage VDD	[Volt]	3.3 typ.
Typical Power Consumption	[Watt]	4.2 typ.
Weight	[Grams]	TBD
Physical Size	[mm]	170.0(W) x 111.0(H) x 7.5(D) (typ.)
Electrical Interface		1 channel LVDS
Surface Treatment		Anti-glare, Hardness 3H
Support Color		262K(6-bit) / 16.2M(8-bit)
Temperature Range Operating Storage (Non-Operating)	[°C]	-30 to +85 (panel surface temperature) -30 to +85
RoHS Compliance		RoHS Compliance



2.2 Optical CharacteristicsThe optical characteristics are measured under stable conditions at 25°C (Room Temperature):

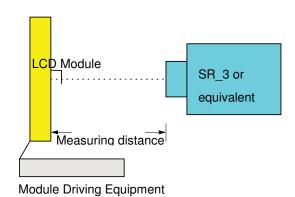
Item	Unit	Conditions	Min.	Тур.	Max.	Note
White Luminance	[cd/m2]	I _F = 80mA (center point)	300	400	-	1
Uniformity	%	TBD	-	-	-	-
Contrast Ratio			-	500	-	2
Response Time	[msec]	Rising	-	20	30	
	[msec]	Falling	-	10	20	3
	[msec]	Raising + Falling	-	30	50	
	[degree]	Horizontal (Right)	70	80	-	
Viewing Angle	[degree]	CR ≧ 10 (Left)	70	80	-	4
Viewing / trigio	[degree]	Vertical (Upper)	50	60	-	4
	[degree]	CR ≥ 10 (Lower)	65	80	-	
		Red x	TBD	TBD	TBD	
		Red y	TBD	TBD	TBD	
Color / Chromaticity		Green x	TBD	TBD	TBD	
Coordinates		Green y	TBD	TBD	TBD	
(CIE 1931)		Blue x	TBD	TBD	TBD	
(CIE 1931)		Blue y	TBD	TBD	TBD	
		White x	TBD	0.31	TBD	
		White y	TBD	0.33	TBD	
Color Gamut	%			60	-	

Note 1: Measurement method

Equipment: Pattern Generator, Power Supply, Digital Voltmeter, Luminance meter (SR_3 or equivalent)

1° with 50cm viewing distance Aperture

Test Point Center Environment < 1 lux

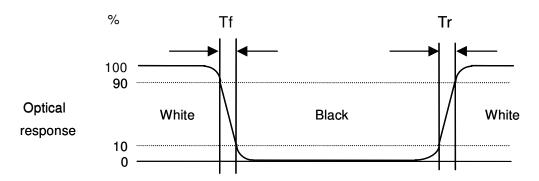




Note 2: Definition of contrast ratio (CR):

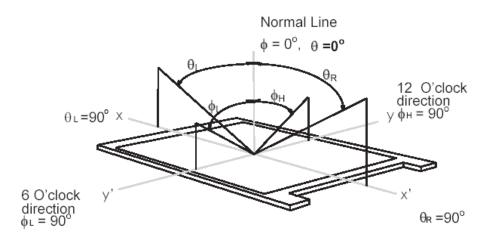
Note 3: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "White" to "Black" (falling time) and from "Black" to "White" (rising time), respectively. The response time interval is between 10% and 90% of amplitudes. Please refer to the figure as below.



Note 4: Definition of viewing angle

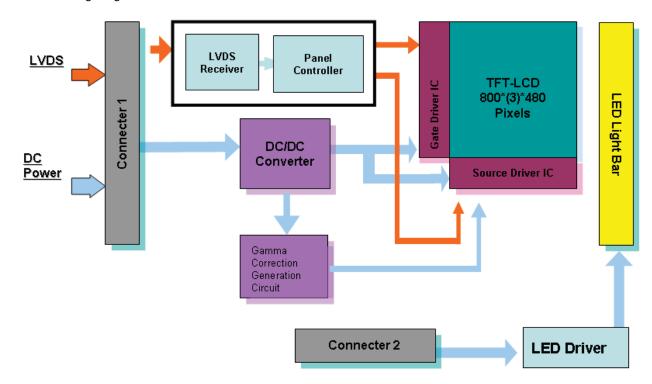
Viewing angle is the measurement of contrast ratio \geq 10, at the screen center, over a 180° horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as below: 90° (θ) horizontal left and right, and 90° (Φ) vertical high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated to its center to develop the desired measurement viewing angle.





3. Functional Block Diagram

The following diagram shows the functional block of the 7.0 inch color TFT/LCD module:





4. Absolute Maximum Ratings

4.1 Absolute Ratings of TFT LCD Module

Item Symbol		Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	VDD	-0.3	+3.6	[Volt]	

4.2 Absolute Ratings of Environment

Item	Symbol	Min	Max	Unit
Operating Temperature	TOP	-30	+85	[°C]
Operation Humidity	HOP	5	90	[%RH]
Storage Temperature	TST	-30	+85	[°C]
Storage Humidity	HST	5	90	[%RH]

Note: Maximum Wet-Bulb should be 39°C and no condensation.



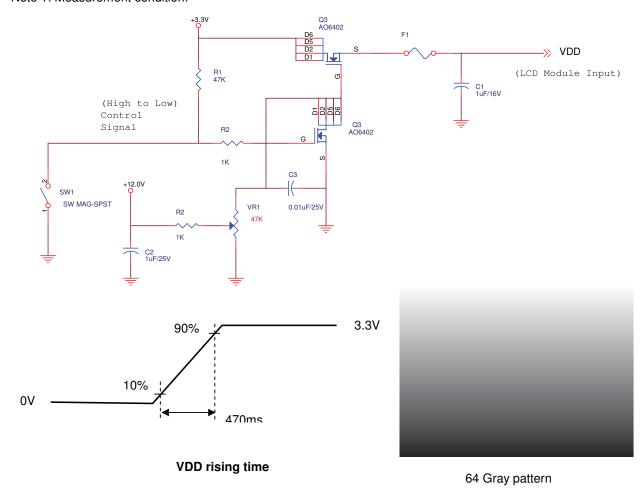
5. Electrical Characteristics

5.1 TFT LCD Module

5.1.1 Power Specification

Symbol	Parameter	Min	Тур	Max	Units	Remark
VDD	Logic/LCD Drive Voltage	3.0	3.3	3.6	[Volt]	± 10%
IDD	VDD Current	-	TBD	TBD	[mA]	64 Gray Bar Pattern (VDD=3.3V, at 60Hz)
Irush	LCD Inrush Current	-	-	TBD	[A]	Note 1
PDD	VDD Power	-	TBD	TBD	[Watt]	64 Gray Bar Pattern (VDD=3.3V, at 60Hz)

Note 1: Measurement condition:



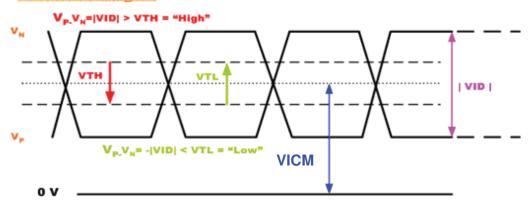


5.1.2 Signal Electrical CharacteristicsInput signals shall be low or Hi-Z state when VDD is off.

Symbol	Item	Min.	Тур.	Max.	Unit	Remark
VTH	Differential Input High Threshold	-	ı	100	[mV]	VICM=1.2V
VTL	Differential Input Low Threshold	-100	-	-	[mV]	VICM=1.2V
VID	Input Differential Voltage	100	400	600	[mV]	
VICM	Differential Input Common Mode Voltage	1.1		1.6	[V]	VTH/VTL=±100mV

Note: LVDS Signal Waveform.

Differential Signal





5.2 Backlight Unit

5.2.1 Parameter guideline for LED(OM)

Following characteristics are measured under a stable condition using an inverter at 25°C (Room Temperature):

Symbol	Parameter	Min	Тур	Max	Unit	Remark
I _F	LED Forward Current	-	80	120	mA	Ta = 25°C
		-	TBD		Volt	$I_F = 80 \text{mA}, \text{ Ta} = -30^{\circ} \text{C}$
V_{F}	LED Forward Voltage	-	32	36.5	Volt	$I_F = 80 \text{mA}, \text{ Ta} = 25^{\circ}\text{C}$
		-	TBD		Volt	I _F = 80mA, Ta = 85°C
P _{LED}	LED Power	-	2.56		Watt	
Operating Life		50000			Hrs	$I_F = 80 \text{mA}, \text{ Ta} = 25^{\circ}\text{C}$

Note 1: Ta means ambient temperature of TFT-LCD module.

- Note 3: If module is driven by high current or at high ambient temperature & humidity condition. The operating life will be reduced.
- Note 4: Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.

Note 2: I_F, V_F are defined for one channel LED. There are total one LED channels in back light unit. See Section 7.2 for LED Backlight Unit Interface Signal Description.



6. Signal Characteristic

6.1 Pixel Format Image

Following figure shows the relationship between input signal and LCD pixel format.

		1			2			7	99		80	00	
1st Line	R	G	В	R	G	В		R	G	В	R	G	В
		•			•				•			•	
		i			;		1		;			·	
					•		1		•			•	
		•			•		1		•			•	
		•			•		1		•			•	
		·			•		•				:		
					•		1						
	Н												
480th Line	R	G	В	R	G	В		R	G	В	R	G	В



6.2 Signal DescriptionLVDS is a differential signal technology for LCD interface and high speed data transfer device. The connector pin definition is as below.

Pin No.	Symbol	Description		
1	VDD	Power Supply, 3.3V (typical)		
2	VDD	Power Supply, 3.3V (typical)		
3	UD	Vertical Reverse Scan Control, When UD=1 or NC → Normal Mode. When UD=0 → Vertical Reverse Scan. Note		
4	LR	Horizontal Reverse Scan Control, When LR=1 or NC → Normal Mode. When LR=0 → Horizontal Reverse Scan. Note		
5	RxIN1-	LVDS differential data input Pair 0		
6	RxIN1+	2720 dinoronial data input i di o		
7	GND	Ground		
8	RxIN2-	LVDS differential data input Pair 1		
9	RxIN2+	LVD3 differential data input Fall F		
10	GND	Ground		
11	RxIN3-	LVDS differential data input Pair 2		
12	RxIN3+	LVD3 dillerential data input Fall 2		
13	GND	Ground		
14	RxCLKIN-	LVDS differential Clock input Pair		
15	RxCLKIN+	LVD3 differential Glock input i all		
16	GND	Ground		
17	SEL 68	LVDS 6/8 bit select function control, Low or NC \rightarrow 6 Bit Input Mode. High \rightarrow 8 Bit Input Mode. Note		
18	NC	NC		
19	RxIN4-	LVDS differential data input Pair 3. Must be tied to Ground in		
20	RxIN4+	6 bit input mode.		

Note : "Low" stands for 0V. "High" stands for 3.3V. "NC" stands for "No Connected."



6.3 The Input Data Format

6.3.1 SEL68

RxIN3

SEL68 = "Low" or "NC" for 6 bits LVDS Input **RxCLKIN** RxIN1 G0 R5 R4 R3 R2 R1 R0 RxIN2 B1 B0 G5 G4 G3 G2 G1

HS

B5

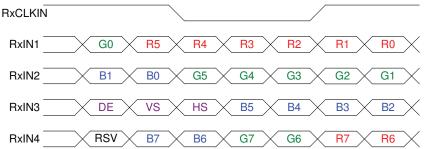
B3

B2

VS

SEL68 = "High" for 8 bits LVDS Input

DE



Note1: Please follow PSWG.

Note2: R/G/B data 7:MSB, R/G/B data 0:LSB

Signal Name	Description	Remark
	•	
R7	Red Data 7 (MSB)	Red-pixel Data
R6	Red Data 6	Each red pixel's brightness data consists of these
R5	Red Data 5	8 bits pixel data.
R4	Red Data 4	
R3	Red Data 3	
R2	Red Data 2	
R1	Red Data 1	
R0	Red Data 0 (LSB)	
G7	Green Data 7 (MSB)	Green-pixel Data
G6	GreenData 6	Each green pixel's brightness data consists of these
G5	GreenData 5	8 bits pixel data.
G4	GreenData 4	
G3	GreenData 3	
G2	GreenData 2	
G1	GreenData 1	
G0	GreenData 0 (LSB)	
B7	Blue Data 7 (MSB)	Blue-pixel Data
B6	Blue Data 6	Each blue pixel's brightness data consists of these
B5	Blue Data 5	8 bits pixel data.
B4	Blue Data 4	·
B3	Blue Data 3	
B2	Blue Data 2	
B1	Blue Data 1	
B0	Blue Data 0 (LSB)	
RxCLKIN+	LVDS Clock Input	
RxCLKIN-	'	
DE	Display Enable	
VS	Vertical Sync	
HS	Horizontal Sync	

Note: Output signals from any system shall be low or Hi-Z state when VDD is off.



6.4 Interface Timing

6.4.1 Timing Characteristics

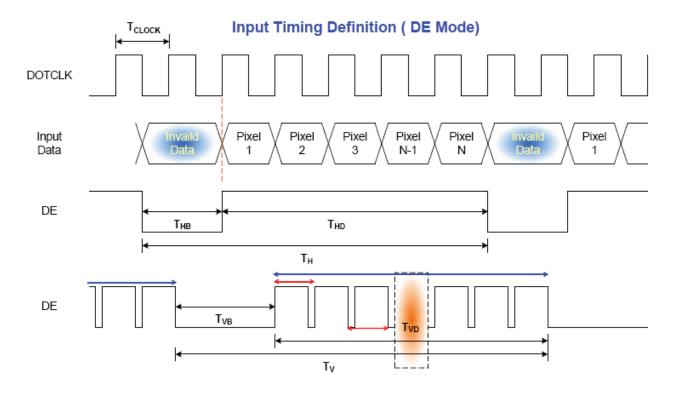
DE mode only

Parameter		Symbol	Min.	Тур.	Max.	Unit	Condition
Clock frequency		1/ T _{Clock}	TBD	32.18	TBD	MHz	
Vertical Section	Period	T _V	TBD	508	TBD		
	Active	T_VD	TBD	480	TBD	T_H	
	Blanking	T_VB	TBD	28	TBD		
Horizontal Section	Period	T _H	TBD	1056	TBD		
	Active	T_{HD}	TBD	800	TBD	T_{Clock}	
	Blanking	T _{HB}	TBD	256	TBD		

Note: Frame rate is 60 Hz.

Note: DE mode.

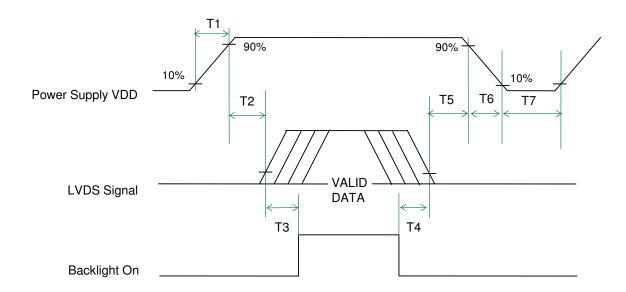
6.4.2 Input Timing Diagram





6.5 Power ON/OFF Sequence

VDD power and lamp on/off sequence is as below. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



Power ON/OFF sequence timing

Devenuentes		11		
Parameter	Min.	Тур.	Max.	Units
T1	0.5	-	10	ms
T2	0	40	50	ms
Т3	200	-	-	ms
T4	200	-	-	ms
T5	0	16	50	ms
Т6	0	-	10	ms
Т7	1000	-	-	ms

The above on/off sequence should be applied to avoid abnormal function in the display. Please make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.

16



7. Connector & Pin Assignment

Physical interface is described as for the connector on module. These connectors are capable of accommodating the following signals and will be following components.

7.1 TFT LCD Signal (CN1): LVDS Connector

Connector Name / Designation	Signal Connector
Manufacturer	Hirose
Connector Model Number	DF19LA-20P-1H or compatible.
Mating Model Number	DF19-20S-1C

Pin No.	Signal Name	Pin No.	Signal Name
1	VDD	2	VDD
3	UD	4	LR
5	RxIN1-	6	RxIN1+
7	GND	8	RxIN2-
9	RxIN2+	10	GND
11	RxIN3-	12	RxIN3+
13	GND	14	RxCKIN-
15	RxCKIN+	16	GND
17	SEL 68	18	NC
19	RxIN4-	20	RxIN4+

7.2 LED Backlight Unit (CN2): Backlight Connector

Connector Name / Designation	Lamp Connector
Manufacturer	Entery
Connector Model Number	3808-F04-01R or compatible.
Mating Model Number	H208K-P04N-02B

Pin#	Symbol	Pin Description
1	VDD12V 12V Power Input	
2	Dimming	Dimming
3	Display_on	Display on/off
4	GND	GND



8. Reliability Test Criteria

Items	Required Condition	Note
Temperature Humidity Bias	40°C/90%,300 hours	
High Temperature Operation	85°C,300 hours	
Low Temperature Operation	-30℃,300 hours	
Hot Storage	85°C,300 hours	
Cold Storage	-30℃,300 hours	
Thermal Shock Test	-20℃/30 min ,60℃/30 min ,100cycles	
Shock Test (Non-Operating)	50G,20ms,Half-sine wave,(±X, ±Y, ±Z)	
Vibration Test	1.5G, (10~200Hz, P-P)	
(Non-Operating)	30 mins/axis (X, Y, Z)	
On/off test	On/10 sec, Off/10 sec, 30,000 cycles	
ESD	Contact Discharge: \pm 8KV, 150pF(330 Ω) 1sec, 8 points, 25 times/ point Air Discharge: \pm 15KV, 150pF(330 Ω) 1sec, 8 points, 25 times/ point	Note 1

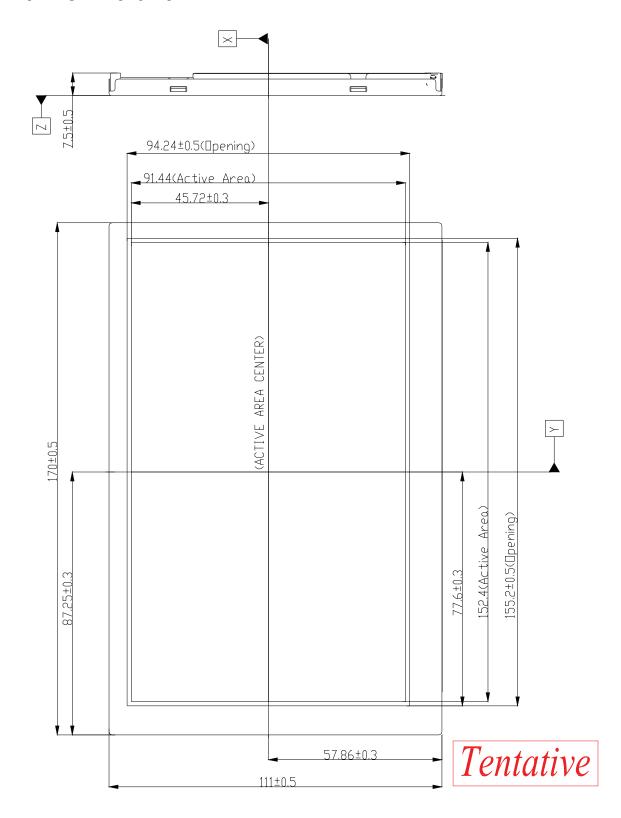
Note1: According to EN61000-4-2, ESD class B: Some performance degradation allowed. No data lost

[.] Self-recoverable. No hardware failures.



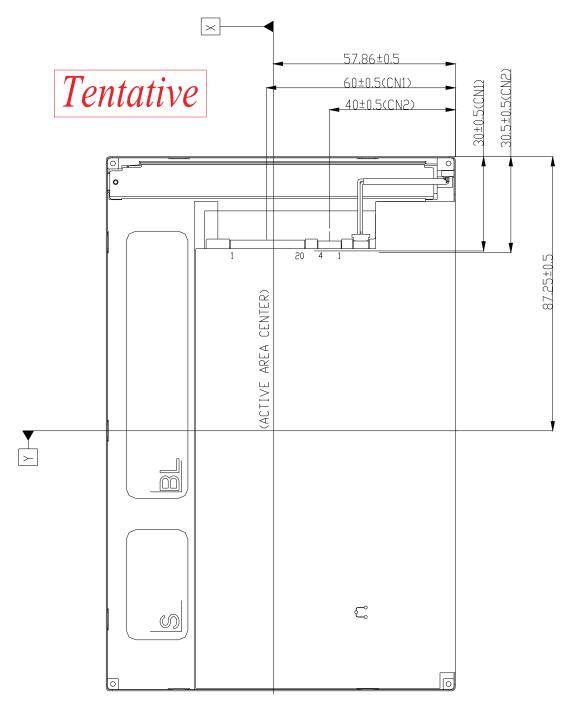
9. Mechanical Characteristics

9.1 LCM Front View





9.2 LCM Rear View



NOTE:
1.LED BLU CONNECTOR TO BE "ENTRY 3808-F0401R (4PIN)" OR COMPATIABLE
2.I/F CONNECTOR TO BE " HIROSE DF19LA-20P-1H " OR COMPATIBLE.



10. Label and Packaging

10.1 Shipping Label (on the rear side of TFT-LCD display)

TBE

10.2 Carton Package

TBD

11 Safety

11.1 Sharp Edge Requirements

There will be no sharp edges or corners on the display assembly that could cause injury.

11.2 Materials

11.2.1 Toxicity

There will be no carcinogenic materials used anywhere in the display module. If toxic materials are used, they will be reviewed and approved by the responsible USMP toxicologist.

11.2.2 Flammability

All components including electrical components that do not meet the flammability grade UL94-V1 in the module will complete the flammability rating exception approval process.

The pRxINted circuit board will be made from material rated 94-V1 or better. The actual UL flammability rating will be pRxINted on the pRxINted circuit board.

11.3 Capacitors

If any polarized capacitors are used in the display assembly, provisions will be made to keep them from being inserted backwards.

11.4 National Test Lab Requirement

The display module will satisfy all requirements for compliance to:

UL 1950, First Edition

U.S.A. Information Technology Equipment