

TFT SPECIFICATION

Part Number	USMP-T043-048027NAS-A0
Size	4.3"
Resolution	480 x 272
Brightness	460 cd/m²
Contrast	600:1
Viewing Angle	60/60/60/60
Operating Temp.	-20 ~ 70°C

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Issue Date	Approved by (customer use)	Checked by	Prepared by

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History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
05/22/2014	01	001	New Drawing.	-	Howard
06/04/2014	01	002	Second Drawing Disp pin internal connect to H Modify Frame Drawing	12 Appendix	Howard
07/15/2014	01	003	New Sample	-	Howard
05/26/2015	01	004	Modify Average Brightness Add LED Life Time Add Rear View	6 9 Appendix	Howard
10/20/2015	01	005	Modify Packaging Specification	Appendix	Howard

Total: 27 Pages



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Note: For detailed information please refer to IC data sheet:

Primacy(TFT LCD): Sitronix--ST7282-G4

(Or compatible IC)



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	480 * 3 (RGB) * 272 Dots
LCD Type	a-Si TFT , Normally white, Transmissive type
Screen size(inch)	4.3 inch
Viewing Direction	6 O'clock
Color configuration	RGB-Strip
Backlight Type	LED B/L
Interface	Digital 24-bits RGB
Other(controller/driver IC)	ST7282-G4 (Or Compatible IC)
ROHS	THIS PRODUCT CONFORMS THE ROHS

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	105.5(W) x 67.2 (L) x 2.6(H) +0,-0.2	mm

LCD panel

Item	Standard Value	Unit
Viewing Area	96.7 (W) * 55.3 (L)	mm
Active Area	95.04 (W) x 53.856 (L)	mm
Pixel Size	0.198 (W) * 0.198 (H)	mm

Note: For detailed information please refer to LCM drawing



1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit
System Power Supply Voltage	VDD	GND=0	-0.3	4.6	V
Operating Temperature	T _{OP}	-	-20	70	°C
Storage Temperature	T _{ST}	-	-30	80	°C
Storage Humidity	H_D	Ta < 60 °C	10	90	%RH

1.4 DC Electrical Characteristics

Module GND = 0V, Ta = 25°C

				011	5 0 V, 1U	20 0
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
	VDD	-	3.0	3.3	3.6	V
Power Supply Voltage	VGH		13	15	16	V
	VGL		-10	-10	-7	V
Input H/L Lovel Voltage	VIH	-	0.7VDD	-	VDD	٧
Input H/L Level Voltage	VIL	-	0	-	0.3VDD	V
Output H/L Level	VOH	-	VDD-0.4	-	VDD	V
Voltage	VOL	-	0	-	GND+0.4	V
Supply Current	I _{DD}	VDD = 3.3 V Pattern=Photo *1	-	25	40	mA

Note1:Maximum current display



1.5 Optical Characteristics

TFT LCD Module

VDD= 3.3 V, Ta=25°C

Item		Symbol	Condition	Min.	Тур.	Max.	unit	-
Response time	Tr+Tf	25	-	-	30	-	ms	-
	Тор	θΥ+	CR ≥ 10	-	60	-		
Viewing angle	Bottom	θΥ-		-	60	-	Dog	Note 4
Viewing angle	Left	θX-	CR ≥ 10	-	60	-	Deg.	Note 4
	Right	θX+		-	60	-		
Contrast rati	0	CR		500	600	-	-	Note 3
	\\/bito	Х		0.24	0.29	0.34		
	White	Y	Ta = 25°C θX , θY = 0°	0.29	0.34	0.39		Note1
Color of CIE	Red	Х		0.54	0.59	0.64		
Color of CIE		Y		0.29	0.34	0.39		
Coordinate (With B/L)	Croon	Х	0, 01 - 0	0.28	0.33	0.38	-	Note
(With D/L)	Green	Y		0.57	0.62	0.67		
	Dluc	Х		0.10	0.15	0.20		
	Blue	Y		0.06	0.11	0.16		
Average Brightr	ness							
Pattern=white display		IV	IF= 20 mA	310	460	-	cd/m2	Note1
(With LCD)*1								
Uniformity (With LCD)*	2	В	IF= 20 mA	70	-	-	%	Note1
(11.1.1.200)								



Note 1:

*1: B=B(min) / B(max) * 100%

*2 : Measurement Condition for Optical Characteristics:

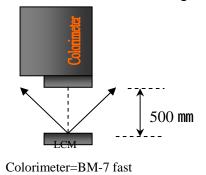
a: Environment: 25 ±5 / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency.

b : Measurement Distance: 500 ± 50 mm , $(\theta = 0^{\circ})$

c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.

d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





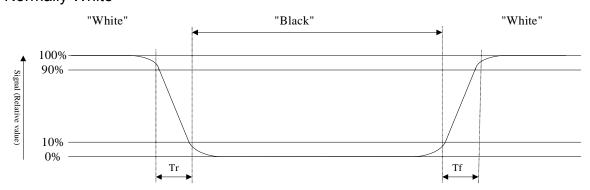
To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:

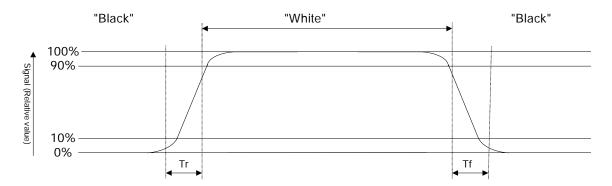
Normally White



6



Normally Black



Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

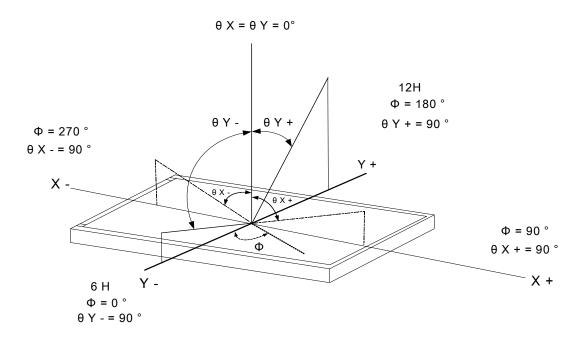
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

Note4: Definition of viewing angle:

Refer to figure as below:





1.6 Backlight Characteristics

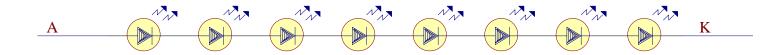
Maximum Ratings

- maximum radingo					
Item	Symbol	Conditions	Min.	Max.	Unit
LED Forward Current	IF	Ta =25		30*1	mA
(Each LED)	II	1a –25	_	30 1	ША
LED Reverse Voltage	VR	Ta =25		5.0	V
(Each LED)	VK	1a -25	-	5.0	V
Power Dissipation	PD	Ta =25	-	90*8	mW
1		1		ı	1

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF		-	25.6	-	V
Average Brightness (Without LCD)	IV	IF=20mA	6500	7800	-	cd/m ²
CIE Color Coordinate	X		-	0.28	-	
(Without LCD)	Υ		-	0.30	-	_
Color	White					
LED Lifetime		20000 Hrs at 25				

B/L Internal Circuit Diagram:





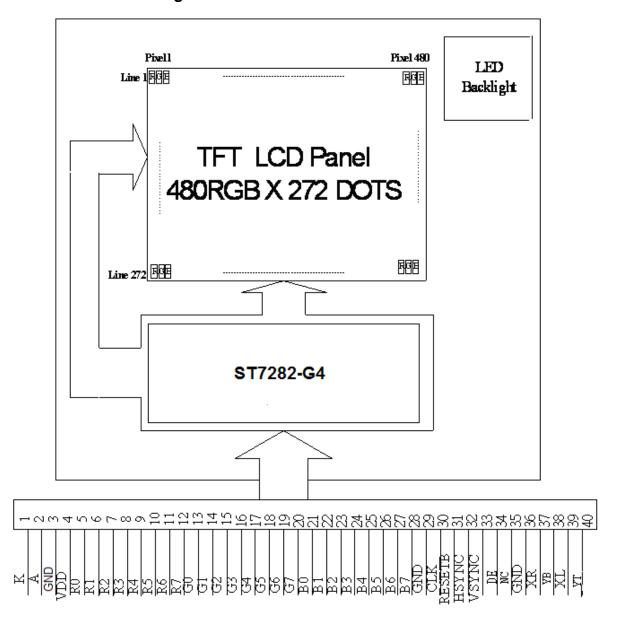
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

Pin No.	Symbol	Function
1	K	Power supply for LED Backlight cathode input
2	А	Power supply for LED Backlight anode input
3	GND	Ground
4	VDD	Digital power
5	R0	Red data bit 0
6	R1	Red data bit 1
7	R2	Red data bit 2
8	R3	Red data bit 3
9	R4	Red data bit 4
10	R5	Red data bit 5
11	R6	Red data bit 6
12	R7	Red data bit 7
13	G0	Green data bit 0
14	G1	Green data bit 1
15	G2	Green data bit 2
16	G3	Green data bit 3
17	G4	Green data bit 4
18	G5	Green data bit 5
19	G6	Green data bit 6
20	G7	Green data bit 7

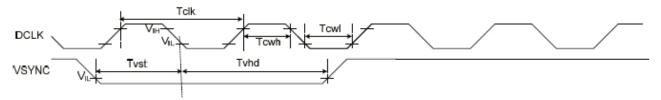


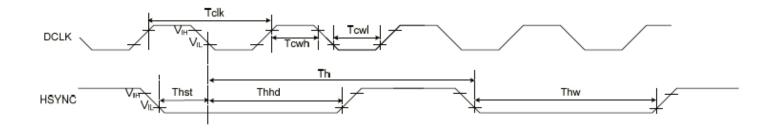
Pin No.	Symbol	Function
21	В0	Blue data bit 0
22	B1	Blue data bit 1
23	B2	Blue data bit 2
24	В3	Blue data bit 3
25	B4	Blue data bit 4
26	B5	Blue data bit 5
27	B6	Blue data bit 6
28	B7	Blue data bit 7
29	GND	Ground
30	CLK	Dot data clock
31	DISP	Display control / standby mode selection "High": Normal display
32	HSYNC	Horizontal sync input
33	VSYNC	Vertical sync input
34	DE	Data input enable. Active High to enable the data input
35	N.C	Not Connect.
36	GND	Ground
37	XR	Not Connect.
38	YB	Not Connect.
39	XL	Not Connect.
40	YT	Not Connect.



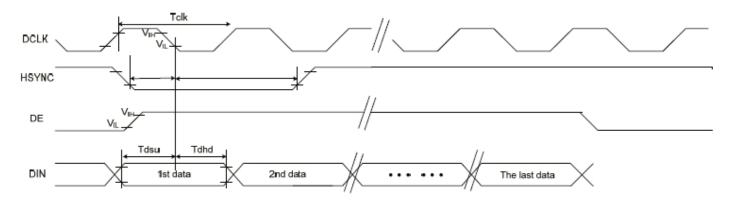
2.3 Timing Characteristics

2.3.1Clock and Data Input Timing





2.3.2 SYNC-DE MODE





ltem	Symbol	Min.	Тур.	Max.	Unit	Conditions	
System operation timing							
VDD power source slew time	TPOR	_	-	20	ms	From 0V to 99% VDD	
GRB pulse width	tRSTW	10	50	-	us	R=10Kohm, C=1uF	
Input/ Output timing		•					
CLK pulse duty	Tcw	40	50	60	%		
Hsync width	Thw	1	-	-	DCLK		
Hsync period	Th	55	60	65	us		
Vsync setup time	Tvst	12	-	-	ns		
Vsync hold time	Tvhd	12	-	_	ns		
Hsync setup time	Thst	12	-	-	ns		
Hsync hold time	Thhd	12	-	-	ns		
Data setup time	Tdsu	12	-	_	ns		
Data hold time	Tdhd	12	-	-	ns		
SD output stable time	Tst	-	-	12	us	Output settled within +20mV	
						Loading = 6.8k+28.2pF.	
GD output rise and fall time	Tgst	_	-	6	us	Output settled (5%~95%),	
						Loading = 4.7k+29.8pF	

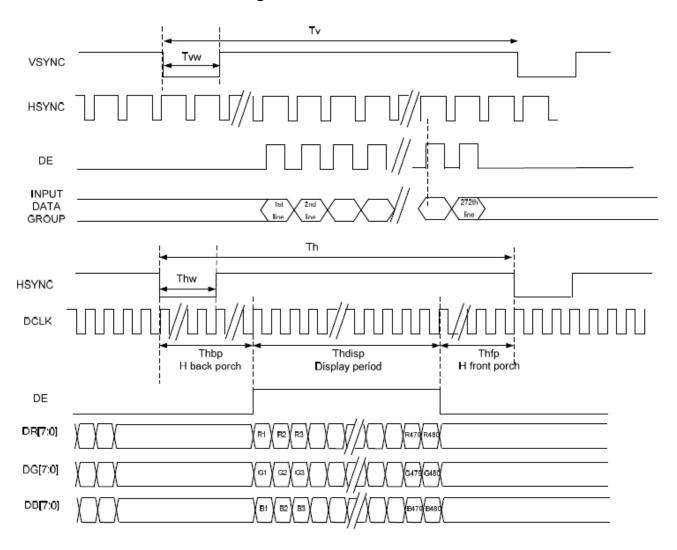


2.3.3Parallel RGB Input Timing Table

	Item	Symbol	Min.	Тур.	Max.	Unit	Remark
DCLK Fred	quency	Fclk	9	12	15	MHz	
DCLK Peri	od	Tclk	67	83	111	ns	
HSYNC	Period Time	Th	485	525	532	DCLK	
	Display Period	Thdisp		480		DCLK	
	Back Porch	Thbp	3	43	50	DCLK	By H_Blanking setting
	Front Porch	Thfp	2	2	2	DCLK	
	Pulse Width	Thw	1	1	1	DCLK	
VSYNC	Period Time	Tv	275	285	303	Н	
	Display Period	Tvdisp		272		Н	
	Back Porch	Tvbp	2	12	30	Н	By V_Blanking setting
	Front Porch	Tvfp	1	1	1	H	
	Pulse Width	Tvw	1	1	1	Н	



2.3.4 SYNC-DE Mode Timing

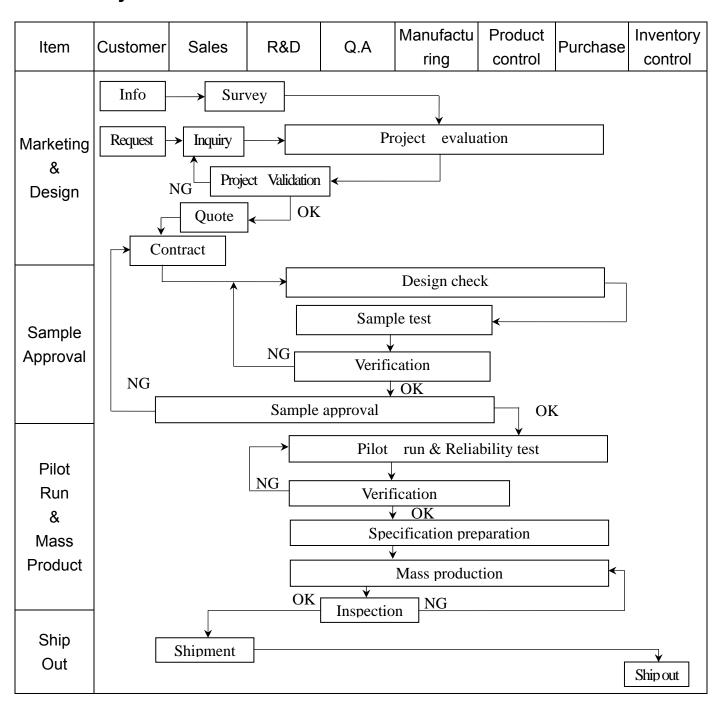


15

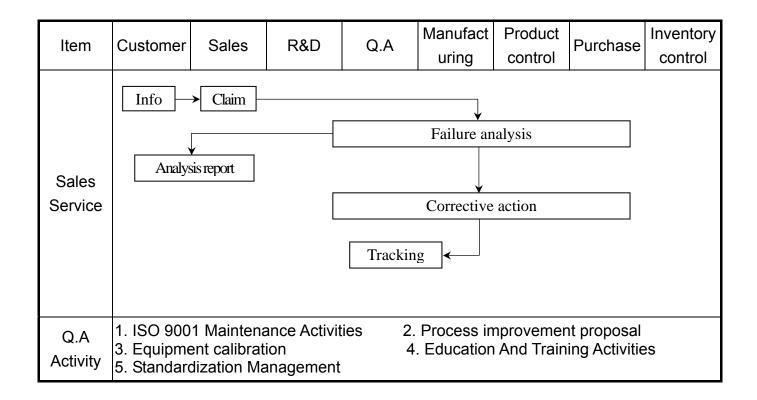


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart







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3.2 Inspection Specification

◆Scope: The document shall be applied to TFT-LCD Module for 3, 5" ~10" (Ver.B01).

◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment: Gauge · MIL-STD · Tester · Sample

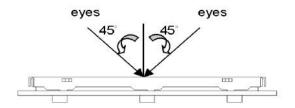
◆Defect Level: Major Defect AQL: 0.4; Minor Defect AQL: 1.5

♦OUT Going Defect Level: Sampling.

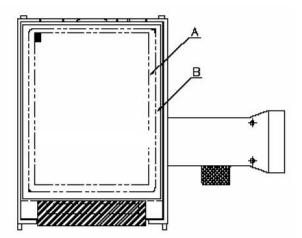
◆Standard of the product appearance test:

a. Manner of appearance test:

- (1). The test best be under 20W×2 fluorescent light, and distance of view must be at 30 cm.
- (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area: viewing area

B area: Outside of viewing area

(4). Standard of inspection: (Unit: mm)



igspace Specification For TFT-LCD Module 3. 5" ~10":

NO	Item			Criteri	on	Level		
			1. 1The part number is inconsistent with work order of production.					
01	Product condition	1. 2 Mix	1. 2 Mixed product types.					
		1. 3 Ass	embled i	n inverse direction.		Major		
02	Quantity	2. 1The	quantity	y is inconsistent with	work order of production.	Major		
03	Outline dimension		oduct di gram.	mension and structu	ire must conform to structu	re Major		
		4. 1 Mis	ssing line	character and icon.		Major		
		4, 2 No function or no display.						
04	Electrical Testing	4. 3 Display malfunction.						
		4. 4 LCD viewing angle defect.						
		4. 5 Current consumption exceeds product specifications.						
				Item	Acceptance (Q'ty)			
	Dot defect			Bright Dot	≦ 4			
	Dot delect		Dot	Dark Dot	≦ 5			
	(Bright dot \		Defect	Joint Dot	≦ 3			
05	Dark dot)			Total	≦ 7	Minor		
	On -display	5. 1 Inspection pattern: full white, full black, Red, Green and						
		blue screens.						
		5. 2 It is defined as dot defect if defect area > 1/2 dot.						
		5. 3 Ine	5. 3 The distance between two dot defect ≥5 mm.					



◆Specification For TFT-LCD Module 3. 5" ~10":

NO	Item		Criterion					
		6. 1 Round type (Non-display or display) :						
		Dimonsion	n (diameten : Ф	Ac	ceptance (Q'ty)		
		Dimension	n (diameter∶Φ	A aı	rea	B area		
	Black or white dot \ scratch \		$\Phi \leq 0.25$	Ign	ore			
	contamination	0.25	$<\Phi \le 0.50$	5		_		
	Round type		Φ > 0.50	0		Ignore		
	→ _X <u>←</u>		Total	5	;			
06	$\Phi = (x+y)/2$	6. 2 Line type(N	on-display or d	isplay) :			Minor	
	(- (-), / -		Length (L) Width (W)		Acceptance (Q'ty			
	Line type	Length (L)			A area	B area		
	/ W		W	≦ 0.03	Ignore			
	→ L +	→ı _L +	L ≦10.0	0.03 < W	≤ 0.05	4		
		L ≦5.0	0.05 < W	≤ 0.10	2	Ignore		
			w	>0.10	As round type	d		
			Total		5			
				Α.	ceptance ((O'tv)		
		Dimension (diameter ∶ Φ)	A ar		B area		
07			$\Phi \le 0.25$		ore			
	Polarizer	0.25 <	$0.25 < \Phi \leq 0.50$				Minor	
	Bubble	0.50 <	$\Phi \leq 0.80$	1		Ignore		
			$\Phi > 0.80$					
		T	otal	5				



◆Specification For TFT-LCD Module 3, 5" ~10":

NO	Item		Criterion		Level
08	The crack of glass	Z: The th	ickness of crack ickness of glass al glass chip: ip on panel surface and cra	Z Y SP [NG]	Minor
		X	Y	z	
		≦ a	Crack can't enter viewing area	≤1/2 t	
		≦ a	Crack can't exceed the half of SP width.	1/2 t < Z ≤2 t	



igspace Specification For TFT-LCD Module 3. 5" ~10":

Symbols: X: The length of crack Z: The thickness of crack T: The thickness of crack T: The thickness of glass 8. 1. 2 Corner crack: $ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	NO	Item	Criterion				
			X : The ler Z : The th t : The thi	ickness of crack ckness of glass	W : ter	minal length	
			X	Y		<u>z</u>	
172 t $<$ Z \leq 2 t \leq 2 t \leq 3 t \leq 4 t \leq 5 t \leq 5 t \leq 6 t \leq 7 t \leq 6 t \leq 7 t \leq 8 t \leq 6 t \leq 7 t \leq 8 t \leq 6 t \leq 7 t \leq 8 t \leq 9 t \leq 1 t \leq 9 t \leq 1 t				Crack can't e			
8. 2. 1 Chip on electrode pad: X Y Y X Y			≦1/5 a		1 / 7	$\mathbf{t} < \mathbf{Z} \leq 2 \mathbf{t}$	
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	08	The crack of glass					Minor
Front $\leq a$ $\leq 1/2 \mathrm{W}$ $\leq t$				7	oad:	Z Y Z	



igspace Specification For TFT-LCD Module 3. 5" ~10":



◆Specification For TFT-LCD Module 3, 5" ~10":

NO	Item	Criterion	Level
		9. 1 Backlight can't work normally.	Major
09	Backlight elements	9. 2 Backlight doesn't light or color is wrong.	Major
		9. 3 Illumination source flickers when lit.	Major
		10. 1 Pin type \ quantity \ dimension must match type in structure diagram.	Major
		10. 2 No short circuits in components on PCB or FPC .	Major
10	General	10.3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
10	appearance	10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤1.5 mm.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

NO.	TEST ITEM	TEST CONDITION				
1	High Temperature Storage Test	Keep in +80 ±2 96 hrs Surrounding temperature, then storage at normal condition 4hrs.				
2	Low Temperature Storage Test	Keep in -30 ±2 96 hrs Surrounding temperature, then storage at normal condition 4hrs.				
3	High Temperature / High Humidity Storage Test	Keep in +60 / 90% R.H duration for 96 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)				
4	Temperature Cycling Storage Test	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$				
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance : 15 35 2. Humidity relative : 30% 60% 3. Energy Storage Capacitance(Cs+Cd) : 150pF±10% 4. Discharge Resistance(Rd) : 330Ω±10% 5. Discharge, mode of operation : Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication : ±5%)				
6	Vibration Test (Packaged)	 Sine wave 10 55 Hz frequency (1 min/sweep) The amplitude of vibration :1.5 mm Each direction (X, Y, Z) duration for 2 Hrs 				
7	Drop Test (Packaged)	Packing Weight (Kg) Drop Height (cm) 0 ~ 45.4 122 45.4 ~ 90.8 76 90.8 ~ 454 61 Over 454 46 Drop Direction: 1 corner / 3 edges / 6 sides each 1 time				



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10 and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25 ±5 and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

