



US Micro Products
Electronic Products for the OEM

LCD PRODUCT SPECIFICATION

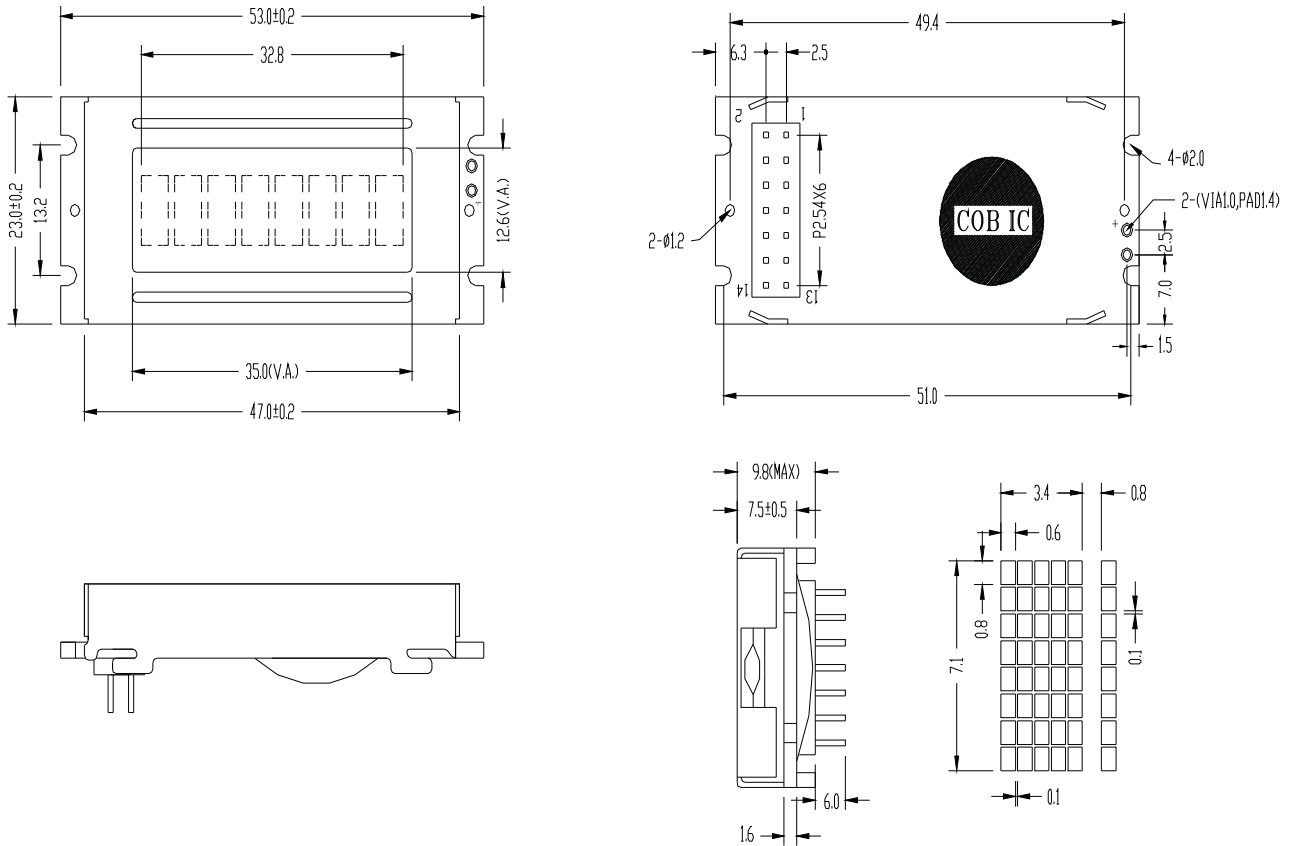
| | |
|---------------------|---|
| PART NUMBER: | USMPC-TQ0801C-PNYA |
| DESCRIPTION: | 8x1 Character LCD; STN Blue Display Mode, Reflective, Positive with No backlight and 6 O'Clock Viewing Direction. |

| ISSUE DATE | APPROVED BY (Customer Use Only) | CHECKED BY | PREPARED BY |
|--------------------------|---|------------|-------------|
| | | | |
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1.Features

1. 5x8 dots
2. Built-in controller (ST7066U-0A or equivalent)
3. +5V power supply
4. Display Mode: STN (Yellow-Green), Reflective
5. Viewing angle: 6:00 O' clock
6. Parallel interface input
7. 1/8 duty
8. Without LED backlight
9. 8x1 character

2.Outline dimension



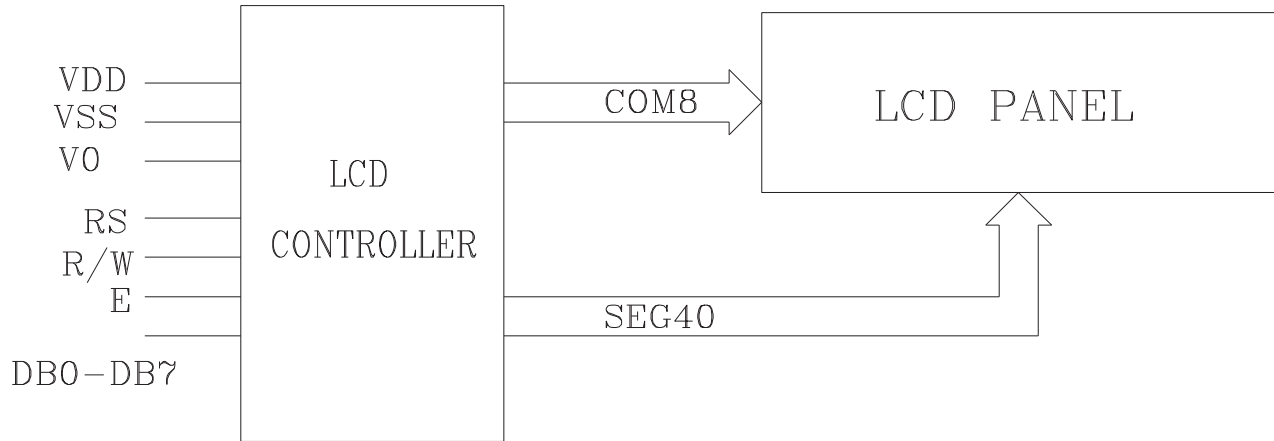
3.Absolute maximum ratings

| Item | Symbol | Standard | | | Unit |
|-----------------------------|----------------------------------|----------|---|----------------------|------|
| Power voltage | V _{DD} -V _{SS} | -0.3 | - | 7.0 | V |
| Input voltage | V _{IN} | -0.3 | - | V _{DD} +0.3 | |
| Operating temperature range | Top | 0 | - | +50 | °C |

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| | | | | | |
|---------------------------|-----|-----|---|-----|--|
| Storage temperature range | Tst | -10 | - | +60 | |
|---------------------------|-----|-----|---|-----|--|

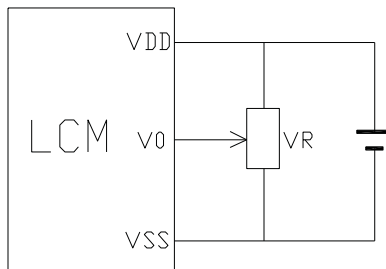
4. Block diagram



5. Interface pin description

| Pin no. | Symbol | External connection | Function |
|---------|-----------------|---------------------|---|
| 1 | V _{SS} | Power supply | Signal ground for LCM (GND) |
| 2 | V _{DD} | | Power supply for logic for LCM |
| 3 | V ₀ | | Contrast adjust |
| 4 | RS | MPU | Register select signal |
| 5 | R/W | MPU | Read/write select signal |
| 6 | E | MPU | Operation (data read/write) enable signal |
| 7~10 | DB0~DB3 | MPU | Four low order bi-directional three-state data bus lines. Used for data transfer between the MPU and the LCM. These four are not used during 4-bit operation. |
| 11~14 | DB4~DB7 | MPU | Four high order bi-directional three-state data bus lines. Used for data transfer between the MPU |

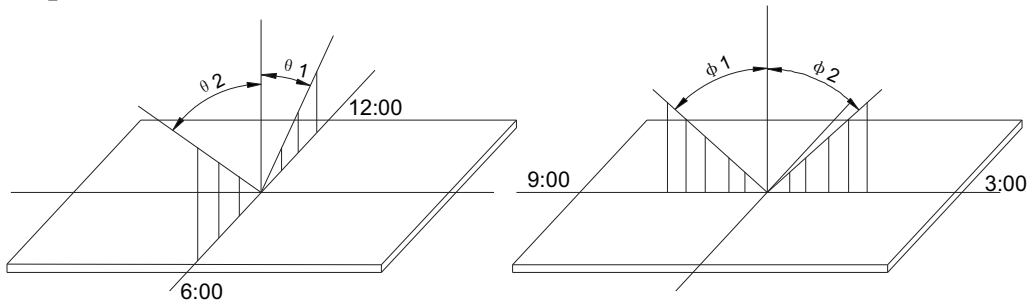
6. Contrast adjust



V_{DD}-V₀: LCD Driving voltage
VR: 10k~20k

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7. Optical characteristics



STN type display module (Ta=25°C, VDD=5.0V)

| Item | Symbol | Condition | Min. | Typ. | Max. | Unit |
|----------------------|------------|--------------|------|------|------|------|
| Viewing angle | $\theta 1$ | $C_r \geq 3$ | | 20 | | deg |
| | $\theta 2$ | | | 40 | | |
| | $\Phi 1$ | | | 35 | | |
| | $\Phi 2$ | | | 35 | | |
| Contrast ratio | C_r | | - | 10 | - | - |
| Response time (rise) | T_r | - | - | 200 | 250 | ms |
| Response time (fall) | T_r | - | - | 300 | 350 | |

8. Electrical characteristics

DC characteristics

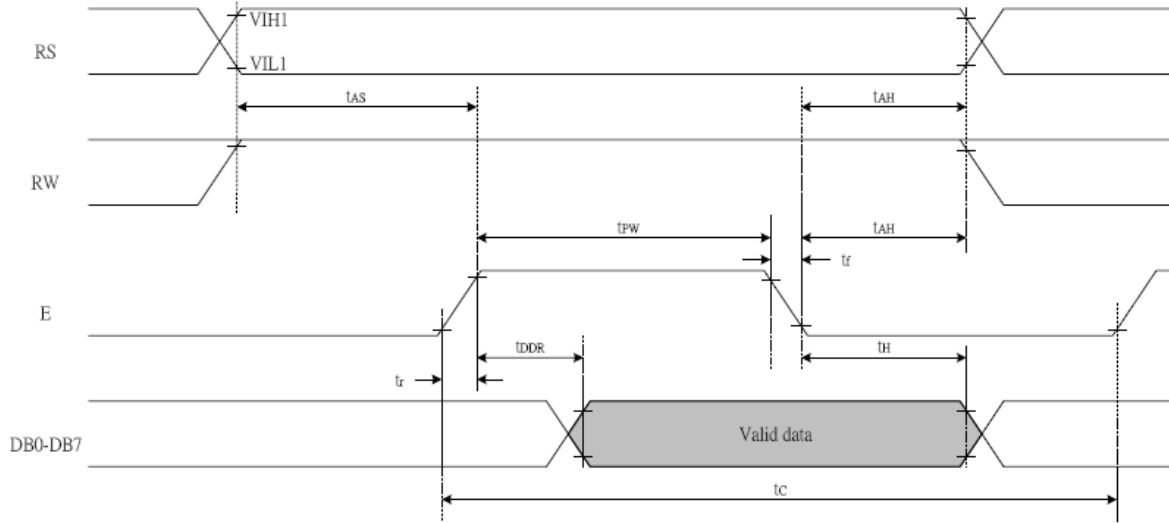
| Parameter | Symbol | Conditions | Min. | Typ. | Max. | Unit |
|--------------------------|--------------|-----------------------------|-------------|------|----------|------|
| Supply voltage for LCD | $V_{DD}-V_0$ | Ta =25°C | - | 4.6 | - | V |
| Input voltage | V_{DD} | | 4.7 | - | 5.5 | |
| Supply current | I_{DD} | Ta=25°C, VDD=5.0V | - | 1.5 | 2.5 | mA |
| Input leakage current | I_{LKG} | | -1 | - | 1.0 | uA |
| “H” level input voltage | V_{IH} | | $0.7V_{DD}$ | - | V_{DD} | V |
| “L” level input voltage | V_{IL} | Twice initial value or less | -0.3 | - | 0.6 | |
| “H” level output voltage | V_{OH} | LOH=-0.1mA | 3.9 | - | V_{DD} | |
| “L” level output voltage | V_{OL} | LOH=0.1mA | - | - | 0.4 | |

Read cycle (Ta=25°C, VDD=5.0V)

| Parameter | Symbol | Test pin | Min. | Typ. | Max. | Unit |
|-----------------------|------------|-----------|------|------|------|------|
| Enable cycle time | t_c | E | 1200 | - | - | ns |
| Enable pulse width | t_{pw} | | 140 | - | - | |
| Enable rise/fall time | t_r, t_f | | - | - | 25 | |
| Address setup time | t_{as} | RS; R/W,E | 0 | - | - | |
| Address hold time | t_{ah} | RS; R/W,E | 10 | - | - | |
| Data setup delay | t_{ddr} | DB0~DB7 | - | - | 100 | |
| Data hold time | t_h | | 10 | - | - | |

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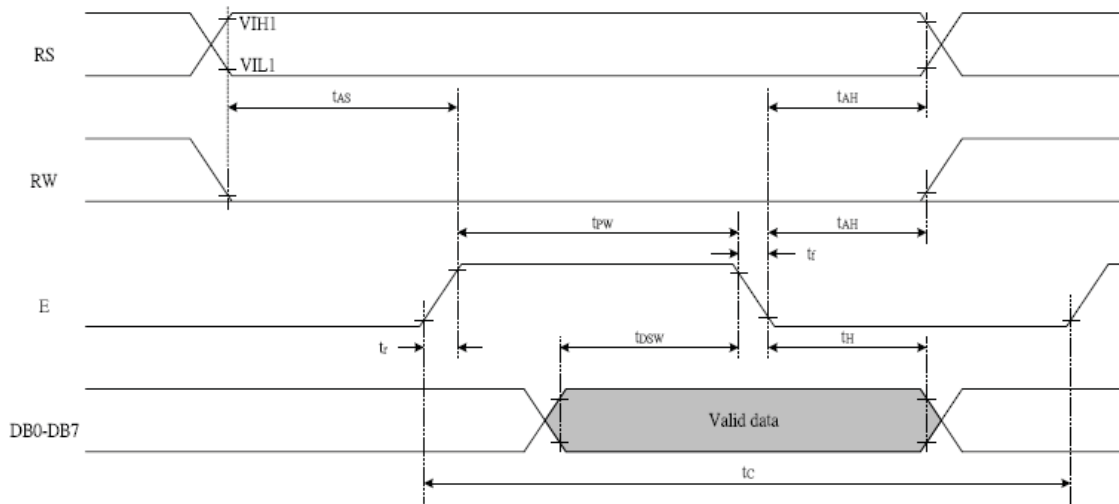
Read mode timing diagram



Write cycle (Ta=25°C, VDD=5.0V)

| Parameter | Symbol | Test pin | Min. | Typ. | Max. | Unit |
|-----------------------|------------|-----------|------|------|------|------|
| Enable cycle time | t_c | E | 1200 | - | - | ns |
| Enable pulse width | t_{pw} | | 140 | - | - | |
| Enable rise/fall time | t_r, t_f | | - | - | 25 | |
| Address setup time | t_{as} | RS; R/W,E | 0 | - | - | |
| Address hold time | t_{ah} | RS; R/W,E | 10 | - | - | |
| Data setup delay | t_{dsw} | DB0~DB7 | 40 | - | - | |
| Data hold time | t_h | | 10 | - | - | |

Write mode timing diagram



9. FUNCTION DESCRIPTION

➤ **System Interface**

This chip has all two kinds of interface type with MPU : 4-bit bus and 8-bit bus. 4-bit bus and 8-bit bus is selected by DL bit in the instruction register.

➤ **Busy Flag (BF)**

When BF = "High", it indicates that the internal operation is being processed. So during this time the next instruction cannot be accepted. BF can be read, when RS = Low and R/W = High (Read Instruction Operation), through DB7 port. Before executing the next instruction, be sure that BF is not high.

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➤ **Address Counter (AC)**

Address Counter (AC) stores DDRAM/CGRAM address, transferred from IR. After writing into (reading from) DDRAM/CGRAM, AC is automatically increased (decreased) by 1. When RS = "Low" and R/W = "High", AC can be read through DB0 - DB6 ports.

➤ **Display Data RAM (DDRAM)**

DDRAM stores display data of maximum 80 x 8 bits (80 characters). DDRAM address is set in the address counter (AC) as a hexadecimal number.

| | | | | | | | | |
|------------------|----|----|----|----|----|----|----|----|
| Display position | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |
| DDRAM address | 00 | 01 | 02 | 03 | 04 | 05 | 06 | 07 |

➤ **CGROM (Character Generator ROM)**

CGROM has a 5 x 8 dots 204 characters pattern and a 5 x 10 dots 32 characters pattern. CGROM has 204 character patterns of 5 x 8 dots.

➤ **CGRAM (Character Generator RAM)**

CGRAM has up to 5 x 8 dot, 8 characters. By writing font data to CGRAM, user defined characters can be used.

| Character Code (DDRAM Data) | | | | | | | | CGRAM Address | | | | | Character Patterns (CGRAM Data) | | | | | | | | | | | |
|-----------------------------|----|----|----|----|----|----|----|---------------|----|----|----|----|---------------------------------|----|----|----|----|----|----|----|----|----|---|---|
| b8 | b7 | b6 | b5 | b4 | b3 | b2 | b1 | b0 | b5 | b4 | b3 | b2 | b1 | b0 | b7 | b6 | b5 | b4 | b3 | b2 | b1 | b0 | | |
| 0 | 0 | 0 | 0 | 0 | - | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | - | - | - | 1 | 1 | 1 | 1 | 1 | | |
| | | | | | | 0 | 0 | 0 | | | | 0 | 0 | 0 | | | | 0 | 0 | 0 | 0 | | | |
| | | | | | | 0 | 0 | 0 | | | | 0 | 1 | 0 | | | | 0 | 0 | 0 | 1 | 0 | 0 | |
| | | | | | | 0 | 0 | 0 | | | | 0 | 0 | 1 | | | | 0 | 0 | 0 | 0 | 1 | 0 | 0 |
| | | | | | | 0 | 0 | 0 | | | | 0 | 0 | 1 | | | | 0 | 0 | 0 | 0 | 1 | 0 | 0 |
| | | | | | | 0 | 0 | 0 | | | | 0 | 0 | 1 | | | | 0 | 1 | 0 | 0 | 1 | 0 | 0 |
| | | | | | | 0 | 0 | 0 | | | | 0 | 0 | 1 | | | | 1 | 0 | 0 | 0 | 1 | 0 | 0 |
| | | | | | | 0 | 0 | 0 | | | | 0 | 0 | 1 | | | | 1 | 1 | 0 | 0 | 0 | 0 | 0 |
| 0 | 0 | 0 | 0 | 0 | - | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | - | - | - | 1 | 1 | 1 | 1 | 0 | | |
| | | | | | | 0 | 0 | 1 | | | | 0 | 0 | 1 | | | | 0 | 0 | 0 | 1 | | | |
| | | | | | | 0 | 0 | 1 | | | | 0 | 1 | 0 | | | | 0 | 0 | 0 | 1 | | | |
| | | | | | | 0 | 0 | 1 | | | | 0 | 1 | 1 | | | | 0 | 0 | 0 | 1 | | | |
| | | | | | | 0 | 0 | 1 | | | | 0 | 1 | 1 | | | | 1 | 0 | 0 | 0 | | | |
| | | | | | | 0 | 0 | 1 | | | | 1 | 0 | 0 | | | | 1 | 0 | 0 | 1 | | | |
| | | | | | | 0 | 0 | 1 | | | | 1 | 0 | 0 | | | | 1 | 0 | 0 | 1 | | | |
| | | | | | | 0 | 0 | 1 | | | | 1 | 1 | 0 | | | | 0 | 0 | 0 | 0 | | | |

Relationship between CGRAM Addresses, Character Codes (DDRAM) and Character patterns (CGRAM Data)

Notes:

- Character code bits 0 to 2 correspond to CGRAM address bits 3 to 5 (3 bits: 8 types).
 - CGRAM address bits 0 to 2 designate the character pattern line position. The 8th line is the cursor position and its display is formed by a logical OR with the cursor. Maintain the 8th line data, corresponding to the cursor display position, at 0 as the cursor display. If the 8th line data is 1, 1 bit will light up the 8th line regardless of the cursor presence.
 - Character pattern row positions correspond to CGRAM data bits 0 to 4 (bit 4 being at the left).
 - As shown Table, CGRAM character patterns are selected when character code bits 4 to 7 are all 0. However, since character code bit 3 has no effect, the R display example above can be selected by either character code 00H or 08H.
 - 1 for CGRAM data corresponds to display selection and 0 to non-selection.
- “-”: Indicates no effect.

➤ **Cursor/Blink Control Circuit**

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It controls cursor/blink ON/OFF at cursor position.

10. Instruction description

Outline

To overcome the speed difference between the internal clock of ST7066U and the MPU clock, ST7066U performs internal operations by storing control in formations to IR or DR. The internal operation is determined according to the signal from MPU, composed of read/write and data bus (Refer to Table7).

Instructions can be divided largely into four groups:

- 1) ST7066U function set instructions (set display methods, set data length, etc.)
- 2) Address set instructions to internal RAM
- 3) Data transfer instructions with internal RAM
- 4) Others

The address of the internal RAM is automatically increased or decreased by 1.

Note: during internal operation, busy flag (DB7) is read “High”.

Busy flag check must be preceded by the next instruction.

Instruction Table

| Instruction | Instruction code | | | | | | | | | | Description | Execution time (fosc= 270 KHZ) | |
|----------------------------|------------------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-------------|--|--------|
| | RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 | | | |
| Clear Display | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | Write “20H” to DDRA and set DDRAM address to “00H” from AC | 1.53ms |
| Return Home | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | - | Set DDRAM address to “00H” From AC and return cursor to Its original position if shifted. The contents of DDRAM are not changed. | 1.53ms |
| Entry mode Set | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | I/D | SH | Assign cursor moving direction And blinking of entire display | 39us |
| Display ON/OFF control | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | D | C | B | Set display (D), cursor (C), and Blinking of cursor (B) on/off Control bit. | |
| Cursor or Display shift | 0 | 0 | 0 | 0 | 0 | 0 | 1 | S/C | R/L | - | - | Set cursor moving and display Shift control bit, and the Direction, without changing of DDRAM data. | 39us |
| Function set | 0 | 0 | 0 | 0 | 0 | 1 | DL | N | F | - | - | Set interface data length (DL: 8 Bit/4 bit), numbers of display Line (N: =2 line/1 line) and, Display font type (F: 5x11/5x8) | 39us |
| Set CGRAM Address | 0 | 0 | 0 | 1 | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 | | Set CGRAM address in address Counter. | 39us |
| Set DDRAM Address | 0 | 0 | 1 | AC6 | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 | | Set DDRAM address in address Counter. | 39us |
| Read busy Flag and Address | 0 | 1 | BF | AC6 | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 | | Whether during internal Operation or not can be known By reading BF. The contents of Address counter can also be read. | 0us |
| Write data to Address | 1 | 0 | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | | Write data into internal RAM (DDRAM/CGRAM). | 43us |
| Read data From RAM | 1 | 1 | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | | Read data from internal RAM (DDRAM/CGRAM). | 43us |

NOTE:

When an MPU program with checking the busy flag (DB7) is made, it must be necessary 1/2fosc is necessary for executing the next instruction by the falling edge of the “E” signal after the busy flag (DB7) goes to “Low”.

Contents

- 1) Clear display

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| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 |

Clear all the display data by writing “20H” (space code) to all DDRAM address, and set DDRAM address to “00H” into AC (address counter).

Return cursor to the original status, namely, bring the cursor to the left edge on the first line of the display.

Make the entry mode increment (I/D=“High”).

2) Return home

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | - |

Return home is cursor return home instruction.

Set DDRAM address to “00H” into the address counter.

Return cursor to its original site and return display to its original status, if shifted.

Contents of DDRAM does not change.

3) Entry mode set

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | I/D | SH |

Set the moving direction of cursor and display.

I/D: increment / decrement of DDRAM address (cursor or blink)

When I/D=“high”, cursor/blink moves to right and DDRAM address is increased by 1.

When I/D=“Low”, cursor/blink moves to left and DDRAM address is increased by 1.

*CGRAM operates the same way as DDRAM, when reading from or writing to CGRAM.

SH: shift of entire display

When DDRAM read (CGRAM read/write) operation or SH=“Low”, shifting of entire display is not performed. If SH=“High” and DDRAM write operation, shift of entire display is performed according to I/D value. (I/D=“high”. shift left, I/D=“Low”. Shift right).

4) Display ON/OFF control

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 0 | 0 | 0 | 0 | 0 | 0 | 1 | D | C | B |

Control display/cursor/blink ON/OFF 1 bit register.

D: Display ON/OFF control bit

When D=“High”, entire display is turned on.

When D=“Low”, display is turned off, but display data remains in DDRAM.

C: cursor ON/OFF control bit

When D=“High”, cursor is turned on.

When D=“Low”, cursor is disappeared in current display, but I/D register preserves its data.

B: Cursor blink ON/OFF control bit

When B=“High”, cursor blink is on, which performs alternately between all the “High” data and display characters at the cursor position.

When B=“Low”, blink is off.

5) Cursor or display shift

| | | | | | | | | | |
|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
| 0 | 0 | 0 | 0 | 0 | 1 | S/C | R/L | - | - |

Shifting of right/left cursor position or display without writing or reading of display data.

This instruction is used to correct or search display data.

During 2-line mode display, cursor moves to the 2nd line after the 40th digit of the 1st line.

Note that display shift is performed simultaneously in all the lines.

When display data is shifted repeatedly, each line is shifted individually.

When display shift is performed, the contents of the address counter are not changed.

Shift patterns according to S/C and R/L bits

| S/C | R/L | Operation |
|-----|-----|---|
| 0 | 0 | Shift cursor to the left, AC is decreased by 1 |
| 0 | 1 | Shift cursor to the right, AC is increased by 1 |
| 1 | 0 | Shift all the display to the left, cursor moves according to the display |
| 1 | 1 | Shift all the display to the right, cursor moves according to the display |

6) Function set

| | | | | | | | | | |
|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
| 0 | 0 | 0 | 0 | 1 | DL | N | F | - | - |

DL: Interface data length control bit

When DL=“High”, it means 8-bit bus mode with MPU.

When DL=“Low”, it means 4-bit bus mode with MPU. Hence, DL is a signal to select 8-bit or 4-bit bus mode.

When 4-bit bus mode, it needs to transfer 4-bit data twice.

N: Display line number control bit

When N=“Low”, 1-line display mode is set.

When N=“High”, 2-line display mode is set.

F: Display line number control bit

When F=“Low”, 5x8 dots format display mode is set.

When F=“High”, 5x11 dots format display mode.

7) Set CGRAM address

| | | | | | | | | | |
|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
| 0 | 0 | 0 | 1 | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 |

Set CGRAM address to AC.

The instruction makes CGRAM data available from MPU.

8) Set DDRAM address

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 0 | 0 | 1 | AC6 | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 |

Set DDRAM address to AC.

This instruction makes DDRAM data available from MPU.

When 1-line display mode (N=LOW), DDRAM address is from “00H” to “4FH”. In 2-line display mode (N=High), DDRAM address in the 1st line is from “00H” to “27H”, and DDRAM address in the 2nd line is from “40H” to “67H”.

9) Read busy flag & address

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 0 | 1 | BF | AC6 | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 |

This instruction shows whether ST7066U is in internal operation or not.

If the resultant BF is “High”, internal operation is in progress and should wait BF is to be LOW, which by then the next instruction can be performed. In this instruction you can also read the value of the address counter.

10) Write data to RAM

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 1 | 0 | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 |

Write binary 8-bit data to DDRAM/CGRAM.

The selection of RAM from DDRAM, and CGRAM, is set by the previous address set instruction (DDRAM address set, CGRAM address set).

RAM set instruction can also determine the AC direction to RAM.

After write operation. The address is automatically increased/decreased by 1, according to the entry mode.

11) Read data from RAM

| RS | R/W | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 1 | 1 | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 |

Read binary 8-bit data from DDRAM/CGRAM.

The selection of RAM is set by the previous address set instruction. If the address set instruction of RAM is not performed before this instruction, the data that has been read first is invalid, as the direction of AC is not yet determined. If RAM data is read several times without RAM address instructions set before, read operation, the correct RAM data can be obtained from the second. But the first data would be incorrect, as there is no time margin to transfer RAM data.

In case of DDRAM read operation, cursor shift instruction plays the same role as DDRAM address set instruction, it also transfers RAM data to output data register.

After read operation, address counter is automatically increased/decreased by 1 according to the entry mode.

After CGRAM read operation, display shift may not be executed correctly.

NOTE: In case of RAM write operation, AC is increased/decreased by 1 as in read operation.

At this time, AC indicates next address position, but only the previous data can be read by the read instruction.

Standard character pattern

| Upper 4bit / Lower 4bit | LLLL | L.LLH | LLHL | LLHH | LHLL | L.HLH | LHHL | L.HHH | HLLL | H.LLH | HLHL | HLHH | HLLL | H.LLH | HHLH | HHHL | HHHH |
|-------------------------|------------|-------|------|------|------|-------|------|-------|------|-------|------|------|------|-------|------|------|------|
| LLLL (1) | CG RAM (1) | | | | | | | | | | | | | | | | |
| LLLH (2) | (2) | | | | | | | | | | | | | | | | |
| LLHL (3) | (3) | | | | | | | | | | | | | | | | |
| LLHH (4) | (4) | | | | | | | | | | | | | | | | |
| LHLL (5) | (5) | | | | | | | | | | | | | | | | |
| LHLH (6) | (6) | | | | | | | | | | | | | | | | |
| LHHL (7) | (7) | | | | | | | | | | | | | | | | |
| LHHH (8) | (8) | | | | | | | | | | | | | | | | |
| HLLL (1) | (1) | | | | | | | | | | | | | | | | |
| HLLH (2) | (2) | | | | | | | | | | | | | | | | |
| HLHL (3) | (3) | | | | | | | | | | | | | | | | |
| HLHH (4) | (4) | | | | | | | | | | | | | | | | |
| HLLL (5) | (5) | | | | | | | | | | | | | | | | |
| HHLH (6) | (6) | | | | | | | | | | | | | | | | |
| HHLH (7) | (7) | | | | | | | | | | | | | | | | |
| HHHL (8) | (8) | | | | | | | | | | | | | | | | |

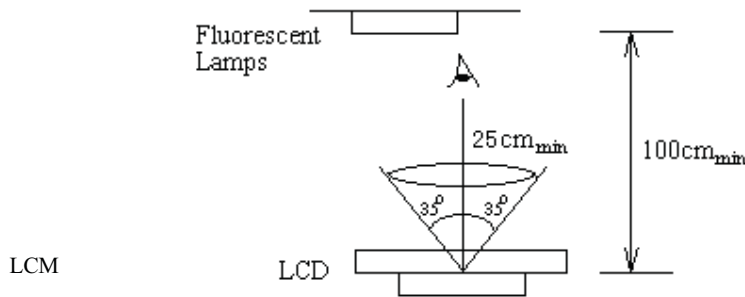
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11.QUALITY SPECIFICATIONS

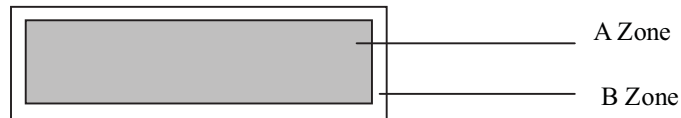
11.1 Standard of the product appearance test

Manner of appearance test: The inspection should be performed in using 20W x 2 fluorescent lamps. Distance between LCM and fluorescent lamps should be 100 cm or more. Distance between LCM and inspector eyes should be 25 cm or more.

Viewing direction for inspection is 35° from vertical against LCM.



Definition of zone:



A Zone: Active display area (minimum viewing area).
B Zone: Non-active display area (outside viewing area).

11.2 Specification of quality assurance

AQL inspection standard

Sampling method: GB2828-87, Level II, single sampling

Defect classification **(Note: * is not including)**


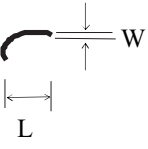
| Classify | Item | Note | AQL | |
|----------------------------|------------------------------|----------------------------|-----------|------|
| Major | Display state | Short or open circuit | 1 | 0.65 |
| | | LC leakage | | |
| | | Flickering | | |
| | | No display | | |
| | Wrong viewing direction | 2 | | |
| | Contrast defect (dim, ghost) | | | |
| | Back-light | 1,8 | | |
| | Non-display | Flat cable or pin reverse | 10 | |
| Wrong or missing component | | 11 | | |
| Minor | Display state | Background color deviation | 2 | 1.0 |
| | | Black spot and dust | 3 | |
| | | Line defect, Scratch | 4 | |
| | | Rainbow | 5 | |
| | | Chip | 6 | |
| | | Pin hole | 7 | |
| | | Polarizer | Protruded | |
| | Bubble and foreign material | | 3 | |
| | Soldering | Poor connection | 9 | |
| | Wire | Poor connection | 10 | |

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| | | |
|-----|----------------------------|----|
| TAB | Position, Bonding strength | 13 |
|-----|----------------------------|----|

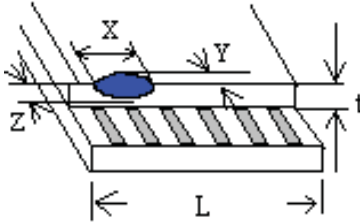
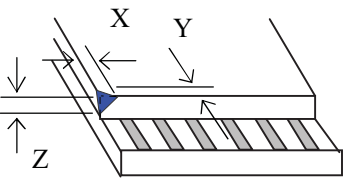
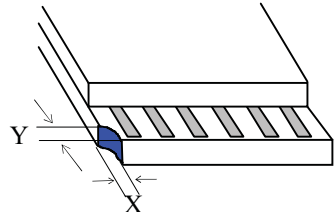
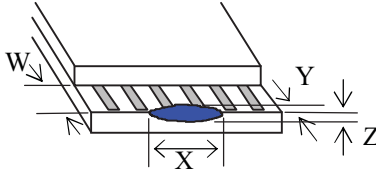
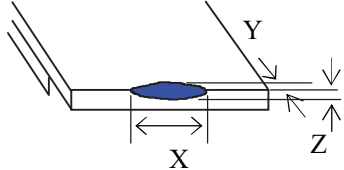
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Note on defect classification

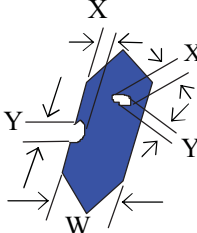
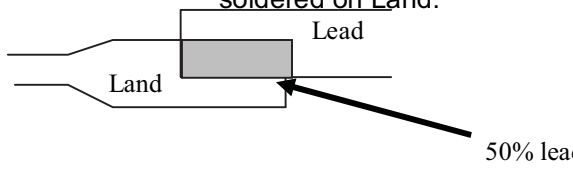
| No. | Item | Criterion | | | | | | | | | | | | |
|-------------------------|--|---|-----------------|-----------------|------------------|-----------|-------------------------|-----|-------------------------|-----------|---------------|------------------|-----------|---------------------|
| 1 | Short or open circuit | Not allow | | | | | | | | | | | | |
| | LC leakage | | | | | | | | | | | | | |
| | Flickering | | | | | | | | | | | | | |
| | No display | | | | | | | | | | | | | |
| | Wrong viewing direction | | | | | | | | | | | | | |
| | Wrong Back-light | | | | | | | | | | | | | |
| 2 | Contrast defect | Refer to approval sample | | | | | | | | | | | | |
| | Backgroundcolor deviation | | | | | | | | | | | | | |
| 3 | Point defect, Black spot, dust (including Polarizer) $\phi = (X+Y)/2$ |  <table border="1" data-bbox="873 819 1279 1054"> <thead> <tr> <th>Point Size</th> <th>Acceptable Qty.</th> </tr> </thead> <tbody> <tr> <td>$\phi \leq 0.10$</td> <td>Disregard</td> </tr> <tr> <td>$0.10 < \phi \leq 0.15$</td> <td>2</td> </tr> <tr> <td>$0.15 < \phi \leq 0.25$</td> <td>1</td> </tr> <tr> <td>$\phi > 0.25$</td> <td>0</td> </tr> </tbody> </table> <p style="text-align: right;">Unit: Inch²</p> | Point Size | Acceptable Qty. | $\phi \leq 0.10$ | Disregard | $0.10 < \phi \leq 0.15$ | 2 | $0.15 < \phi \leq 0.25$ | 1 | $\phi > 0.25$ | 0 | | |
| | | Point Size | Acceptable Qty. | | | | | | | | | | | |
| $\phi \leq 0.10$ | Disregard | | | | | | | | | | | | | |
| $0.10 < \phi \leq 0.15$ | 2 | | | | | | | | | | | | | |
| $0.15 < \phi \leq 0.25$ | 1 | | | | | | | | | | | | | |
| $\phi > 0.25$ | 0 | | | | | | | | | | | | | |
| 4 | Line defect, Scratch |  <table border="1" data-bbox="808 1270 1315 1438"> <thead> <tr> <th colspan="2">Line</th> <th rowspan="2">Acceptable Qty.</th> </tr> <tr> <th>L</th> <th>W</th> </tr> </thead> <tbody> <tr> <td>---</td> <td>$0.05 > W$</td> <td rowspan="3">Disregard</td> </tr> <tr> <td>$3.0 > L$</td> <td>$0.1 > W > 0.05$</td> </tr> <tr> <td>$2.0 > L$</td> <td>$0.15 \geq W > 0.1$</td> </tr> </tbody> </table> <p style="text-align: right;">Unit: mm</p> | Line | | Acceptable Qty. | L | W | --- | $0.05 > W$ | Disregard | $3.0 > L$ | $0.1 > W > 0.05$ | $2.0 > L$ | $0.15 \geq W > 0.1$ |
| Line | | Acceptable Qty. | | | | | | | | | | | | |
| L | W | | | | | | | | | | | | | |
| --- | $0.05 > W$ | Disregard | | | | | | | | | | | | |
| $3.0 > L$ | $0.1 > W > 0.05$ | | | | | | | | | | | | | |
| $2.0 > L$ | $0.15 \geq W > 0.1$ | | | | | | | | | | | | | |
| 5 | Rainbow | Not more than two color changes across the viewing area. | | | | | | | | | | | | |

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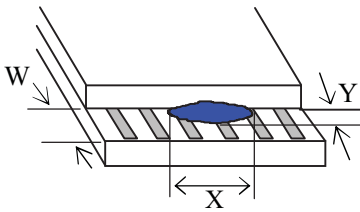
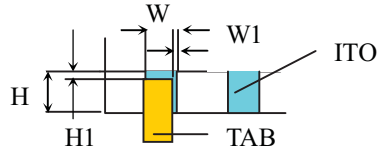
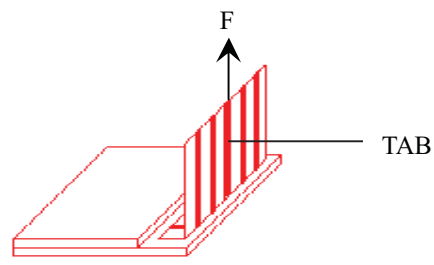
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| No | Item | Criterion | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
|------------------------|---|--|---|---|---|---------|-------|------------|---|---|---|----------|-------|----------|---|---|---|----------|----------|----------|------------------------|--|--|---|---|---|-----------|------------|----------|---|---|---|----------|----------|------------|
| 6 | <p>Chip</p> <p>Remark:</p> <p>X: Length direction</p> <p>Y: Short direction</p> <p>Z: Thickness direction</p> <p>t: Glass thickness</p> <p>W: Terminal Width</p> <p>L: Glass length</p> |  <p>Acceptable criterion</p> <table border="1" data-bbox="933 325 1299 399"> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> <tr> <td>$< L/8$</td> <td>0.5mm</td> <td>$\leq t/2$</td> </tr> </table>  <p>Acceptable criterion</p> <table border="1" data-bbox="925 619 1307 693"> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> <tr> <td>≤ 2</td> <td>0.5mm</td> <td>$\leq t$</td> </tr> </table>  <p>Acceptable criterion</p> <table border="1" data-bbox="941 892 1307 997"> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> <tr> <td>≤ 3</td> <td>≤ 2</td> <td>$\leq t$</td> </tr> <tr> <td colspan="2">shall not reach to ITO</td> <td></td> </tr> </table>  <p>Acceptable criterion</p> <table border="1" data-bbox="933 1249 1307 1323"> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> <tr> <td>Disregard</td> <td>≤ 0.2</td> <td>$\leq t$</td> </tr> </table>  <p>Acceptable criterion</p> <table border="1" data-bbox="933 1512 1274 1585"> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> <tr> <td>≤ 5</td> <td>≤ 2</td> <td>$\leq t/3$</td> </tr> </table> | X | Y | Z | $< L/8$ | 0.5mm | $\leq t/2$ | X | Y | Z | ≤ 2 | 0.5mm | $\leq t$ | X | Y | Z | ≤ 3 | ≤ 2 | $\leq t$ | shall not reach to ITO | | | X | Y | Z | Disregard | ≤ 0.2 | $\leq t$ | X | Y | Z | ≤ 5 | ≤ 2 | $\leq t/3$ |
| X | Y | Z | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| $< L/8$ | 0.5mm | $\leq t/2$ | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| X | Y | Z | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| ≤ 2 | 0.5mm | $\leq t$ | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| X | Y | Z | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| ≤ 3 | ≤ 2 | $\leq t$ | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| shall not reach to ITO | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| X | Y | Z | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Disregard | ≤ 0.2 | $\leq t$ | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| X | Y | Z | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| ≤ 5 | ≤ 2 | $\leq t/3$ | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

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| No. | Item | Criterion | | | | | | | | |
|-------------------------|---|--|------------|----------------|------------------|-----------|-------------------------|---|---------------|---|
| 7 | Segment pattern $W = \text{Segment width}$ $\phi = (X+Y)/2$ | (1) Pin hole $\phi < 0.10\text{mm}$ is acceptable.  <table border="1" data-bbox="868 430 1295 592"> <thead> <tr> <th>Point Size</th> <th>Acceptable Qty</th> </tr> </thead> <tbody> <tr> <td>$\phi \leq 1/4W$</td> <td>Disregard</td> </tr> <tr> <td>$1/4W < \phi \leq 1/2W$</td> <td>1</td> </tr> <tr> <td>$\phi > 1/2W$</td> <td>0</td> </tr> </tbody> </table> <p style="text-align: right;">Unit: mm</p> | Point Size | Acceptable Qty | $\phi \leq 1/4W$ | Disregard | $1/4W < \phi \leq 1/2W$ | 1 | $\phi > 1/2W$ | 0 |
| Point Size | Acceptable Qty | | | | | | | | | |
| $\phi \leq 1/4W$ | Disregard | | | | | | | | | |
| $1/4W < \phi \leq 1/2W$ | 1 | | | | | | | | | |
| $\phi > 1/2W$ | 0 | | | | | | | | | |
| 8 | Back-light | (1) The color of backlight should correspond its specification. (2) Not allow flickering | | | | | | | | |
| 9 | Soldering | (1) Not allow heavy dirty and solder ball on PCB. (The size of dirty refer to point and dust defect) (2) Over 50% of lead should be soldered on Land.  | | | | | | | | |
| 10 | Wire | (1) Copper wire should not be rusted (2) Not allow crack on copper wire connection. (3) Not allow reversing the position of the flat cable. (4) Not allow exposed copper wire inside the flat cable. | | | | | | | | |
| 11* | PCB | (1) Not allow screw rust or damage. (2) Not allow missing or wrong putting of component. | | | | | | | | |

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| No | Item | Criterion |
|----|--------------------------------|---|
| 12 | Protruded W: Terminal Width |  <p>Acceptable criteria: $Y \leq 0.4$</p> |
| 13 | TAB | <p>1. Position</p>  <div style="border: 1px solid black; padding: 5px; width: fit-content; margin-left: auto; margin-right: auto;"> $W1 \leq 1/3 W$ $H1 \leq 1/3 H$ </div> <p>2. TAB bonding strength test</p>  <p>$P (=F/TAB \text{ bonding width}) \geq 650\text{gf/cm}$,(speed rate: 1mm/min) 5pcs per SOA (shipment)</p> |
| 14 | Total no. of acceptable Defect | <p>A. Zone</p> <p>Maximum 2 minor non-conformities per one unit. Defect distance: each point to be separated over 10mm</p> <p>B. Zone</p> <p>It is acceptable when it is no trouble for quality and assembly in customer's end product.</p> |

11.3 Reliability of LCM

Reliability test condition:

| Item | Condition | Time (hrs) | Assessment |
|----------------------|---|------------|--|
| High temp. Storage | 80°C | 48 | No abnormalities in functions and appearance |
| High temp. Operating | 70°C | 48 | |
| Low temp. Storage | -30°C | 48 | |
| Low temp. Operating | -20°C | 48 | |
| Humidity | 40°C/ 90%RH | 48 | |
| Temp. Cycle | 0°C ← 25°C → 50°C (30 min ← 5 min → 30min) | 10cycles | |

Recovery time should be 24 hours minimum. Moreover, functions, performance and appearance shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature (20±8°C), normal humidity (below 65% RH), and in the area not exposed to direct sun light.

11.4 Precaution for using LCD/LCM

LCD/LCM is assembled and adjusted with a high degree of precision. Do not attempt to make any alteration or modification. The followings should be noted.

General Precautions:

1. LCD panel is made of glass. Avoid excessive mechanical shock or applying strong pressure onto the surface of display area.
2. The polarizer used on the display surface is easily scratched and damaged. Extreme care should be taken when handling. To clean dust or dirt off the display surface, wipe gently with cotton, or other soft material soaked with isopropyl alcohol, ethyl alcohol or trichlorotrifluoroethane, do not use water, ketone or aromatics and never scrub hard.
3. Do not tamper in any way with the tabs on the metal frame.
4. Do not make any modification on the PCB without consulting USMP.
5. When mounting a LCM, make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
6. Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels and also cause rainbow on the display.
7. Be careful not to touch or swallow liquid crystal that might leak from a damaged cell. Any liquid crystal adheres to skin or clothes, wash it off immediately with soap and water.

STATIC ELECTRICITY PRECAUTIONS:

1. CMOS-LSI is used for the module circuit; therefore operators should be grounded whenever he/she comes into contact with the module.
2. Do not touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface

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terminals with any parts of the human body.

3. Do not touch the connection terminals of the display with bare hand; it will cause disconnection or defective insulation of terminals.
4. The modules should be kept in anti-static bags or other containers resistant to static for storage.
5. Only properly grounded soldering irons should be used.
6. If an electric screwdriver is used, it should be grounded and shielded to prevent sparks.
7. The normal static prevention measures should be observed for work clothes and working benches.
8. Since dry air is inductive to static, a relative humidity of 50-60% is recommended.

SOLDERING PRECAUTIONS:

1. Soldering should be performed only on the I/O terminals.
2. Use soldering irons with proper grounding and no leakage.
3. Soldering temperature: $280^{\circ}\text{C}\pm 10^{\circ}\text{C}$
4. Soldering time: 3 to 4 second.
5. Use eutectic solder with resin flux filling.
6. If flux is used, the LCD surface should be protected to avoid spattering flux.
7. Flux residue should be removed.

OPERATION PRECAUTIONS:

1. The viewing angle can be adjusted by varying the LCD driving voltage V_o .
2. Since applied DC voltage causes electro-chemical reactions, which deteriorate the display, the applied pulse waveform should be a symmetric waveform such that no DC component remains. Be sure to use the specified operating voltage.
3. Driving voltage should be kept within specified range; excess voltage will shorten display life.
4. Response time increases with decrease in temperature.
5. Display color may be affected at temperatures above its operational range.
6. Keep the temperature within the specified range usage and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel-off or generate bubbles.
7. For long-term storage over 40°C is required, the relative humidity should be kept below 60%, and avoid direct sunlight.



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