



**US Micro Products**  
*Electronic Products for the OEM*

# TFT-LCD PRODUCT SPECIFICATION

<b>PART NUMBER:</b>	<b>USMP-TT014SQ-01D</b>
<b>DESCRIPTION:</b>	1.44" TFT LCD with 128 x 128 resolution, 8080 MCU Parallel System interface, and White LED backlight.

ISSUE DATE	APPROVED BY (Customer Use Only)	CHECKED BY	PREPARED BY
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# 1. SPECIFICATIONS

## 1.1 Features

### Main LCD Panel

Item	Standard Value
Display Type	128(R · G · B) * 128 Dots
LCD Type	Normally white TN, Transmissive type
Screen size(inch)	1.4" inch
Viewing Direction	12 O'clock
Color configuration	R.G.B. vertical stripe
Backlight	White LED
Interface	8Bits data bus (8080 MCU Parallel System interface)
Other(controller / driver IC)	Sitronix: ST7715R

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## 1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	31.1 (W)*36.9 (L) *2.85(H)	mm

### TFT LCD Panel

Item	Standard Value	Unit
Viewing Area	26.5 (W) *27.5 (L)	mm
Active Area	25.5 (W) * 26.5 (L)	mm

Note : For detailed information please refer to LCM drawing.

### 1.3 Absolute Maximum Ratings

#### Module

Item	Symbol	Condition	Min.	Max.	Unit
Supply Voltage	VDD	-	-0.3	+4.8	V
Supply Voltage(Logic)	VDDI	-	-0.3	+4.6	V
Supply Voltage (Digital)	VCC	-	-0.3	+1.95	V
Driver supply voltage	VGH-VGL	-	-0.3	+30	V
Logic input voltage range	VIN	-	-0.3	VDDI+0.3	V
Logic output voltage range	VO	-	-0.3	VDDI+0.3	
Operating Temperature	TOP	-	-20	+70	°C
Storage Temperature	TST	-	-30	+80	°C
Storage Humidity	HD	Ta ≅ 60 °C	0	90	%RH

### 1.4 DC Electrical Characteristics

#### Module

GND = 0V, Ta = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
System voltage	VDD	-	2.7	3.0	3.3	V
Interface operation voltage	VDDI	-	1.65	1.8	3.7	
Logic-high input voltage	VIH	-	0.7VDDI	-	VDDI	V
Logic-low input voltage	VIL	-	VSS	-	0.3VDDI	V
Logic-high output voltage	VOH	IOH=-1.0mA	0.8VDDI	-	VDDI	V
Logic-low output voltage	VOL	IOL=+1.0mA	VSS	-	0.2VDDI	V
Supply Current	IDD	VDD=3.0V, Pattern=Word*1	-	0.80	1.20	mA

Note1 : Maximum current display.

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### 1.5 Optical Characteristics

TFT LCD Panel

VDD =3.0V, Ta=25°C

Item	Symbol	Condition	Min.	Typ.	Max.	unit		
Response time	Tr + Tf	Ta = 25°C θX, θY = 0°	-	30	45	ms	Note2	
Viewing angle	Top	θY+	CR≥10	-	45	-	Deg.	Note4
	Bottom	θY-		-	15	-		
	Left	θX-		-	45	-		
	Right	θX+		-	45	-		
Contrast ratio	CR		150	200	-	-	Note3	
Color of CIE Coordinate (With B/L)	White	X	Ta = 25°C θX , θY = 0°	0.23	0.28	0.33	-	Note1
		Y		0.24	0.29	0.34		
	Red	X		0.54	0.59	0.64		
		Y		0.26	0.31	0.36		
	Green	X		0.26	0.31	0.36		
		Y		0.51	0.56	0.61		
	Blue	X		0.09	0.14	0.19		
		Y		0.03	0.08	0.13		
Average Brightness Pattern=white display (With B/L)	IV	IF=20mA	240	350	-	cd/m2	Note1	
Uniformity (With B/L)	△B	IF=20mA	80	-	-	%	Note1	

Note1:

1 :  $\Delta B = B(\min) / B(\max) \times 100\%$ .

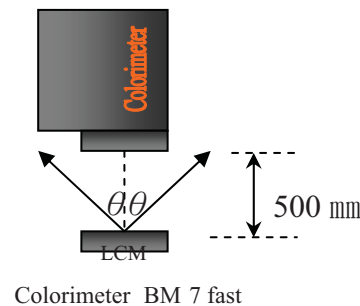
2 : Measurement Condition for Optical Characteristics:

a : Environment: 25°C ±5°C / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency.

b : Measurement Distance: 500 ± 50 mm, (θ= 0°).

c : Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.

d : The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%.

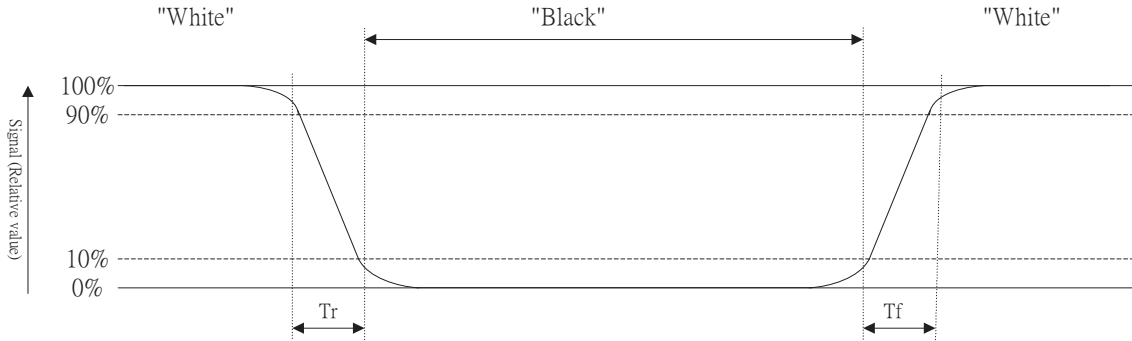


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Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from “black” to “white”(falling time) and from “white” to “black”(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:



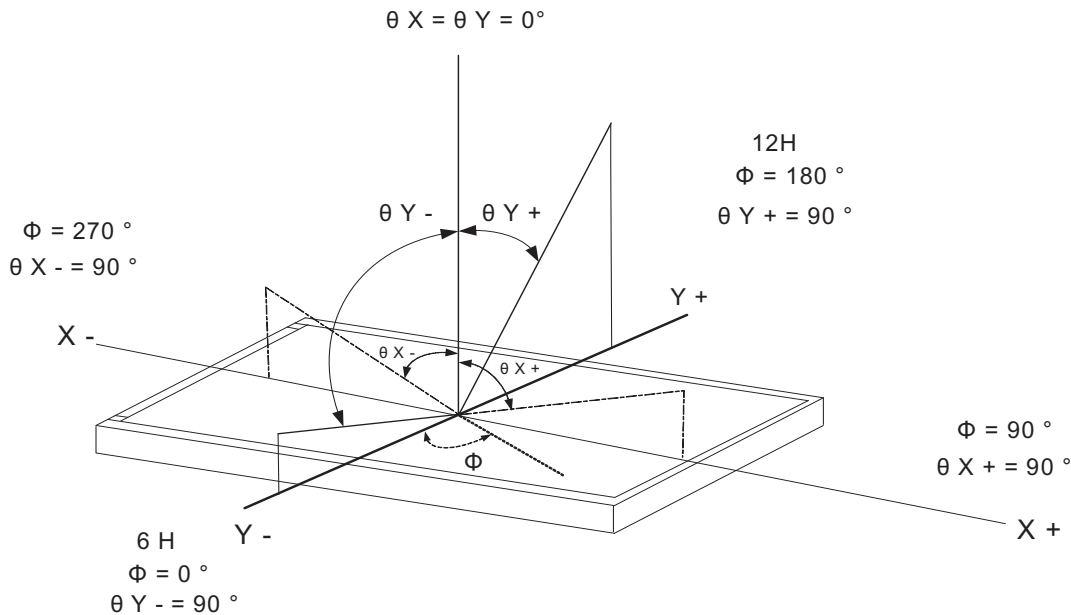
Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector output when LCD is at "Black" state}}$$

Note4: Definition of viewing angle:

Refer to figure as below:



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## 1.6 Backlight Characteristics

### Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°C	-	30	mA
Reverse Voltage	VR	Ta =25°C	-	5.0	V
Power Dissipation	PD	Ta =25°C	-	90	mW

### Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Voltage	VF	IF= 20mA	-	3.2	3.6	V
Average Brightness (with LCD)	IV	IF= 20mA	1700	2100	-	cd/m <sup>2</sup>
Color of CIE Coordinate (with LCD)	X	IF= 20mA	0.26	0.29	0.315	-
	Y		0.26	0.29	0.315	
Color	White					

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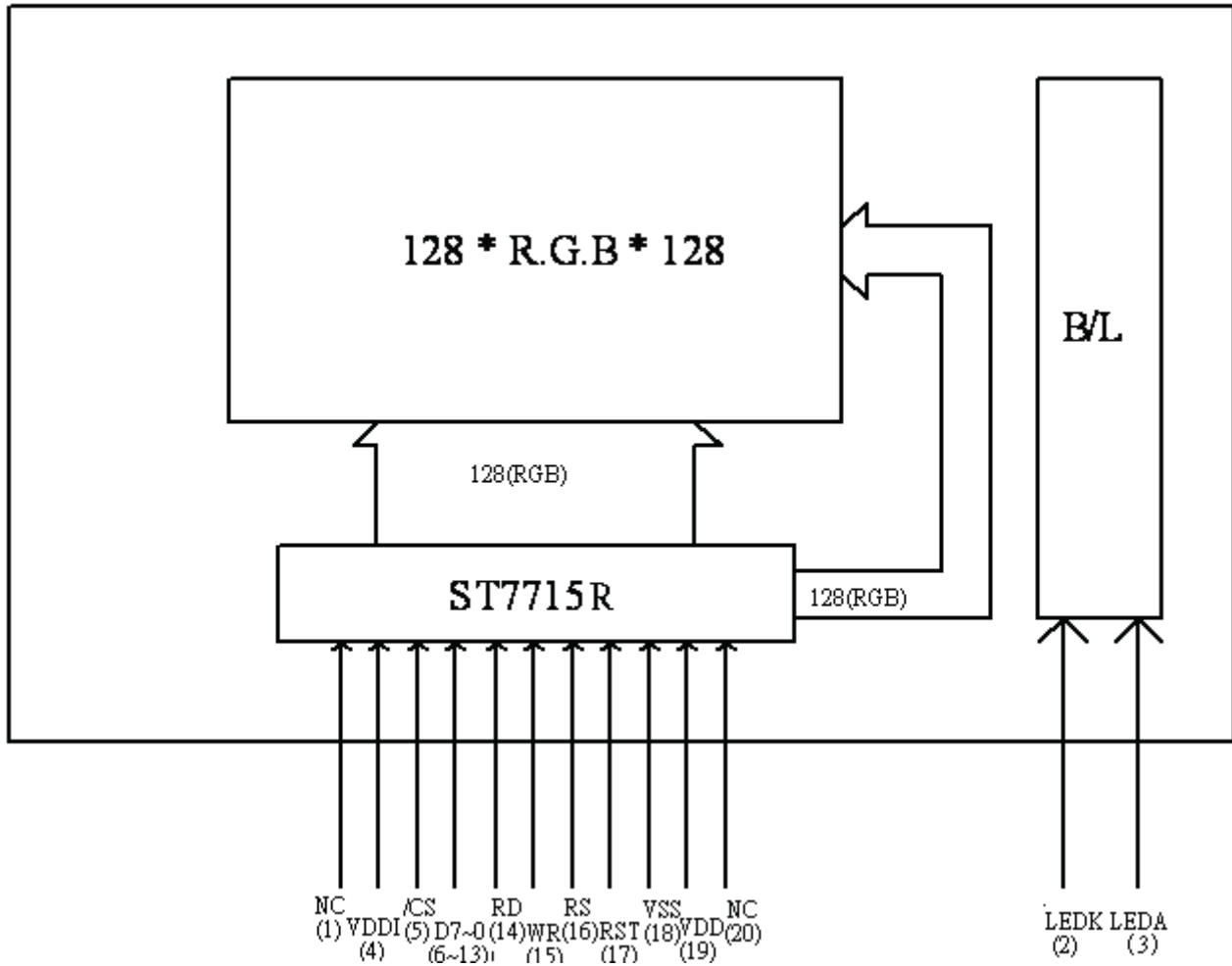
## 2. MODULE STRUCTURE

### 2.1 Counter Drawing

#### 2.1.1 LCM Mechanical Diagram

\* See Appendix

#### 2.1.2 Block Diagram



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## 2.2 Interface Pin Description

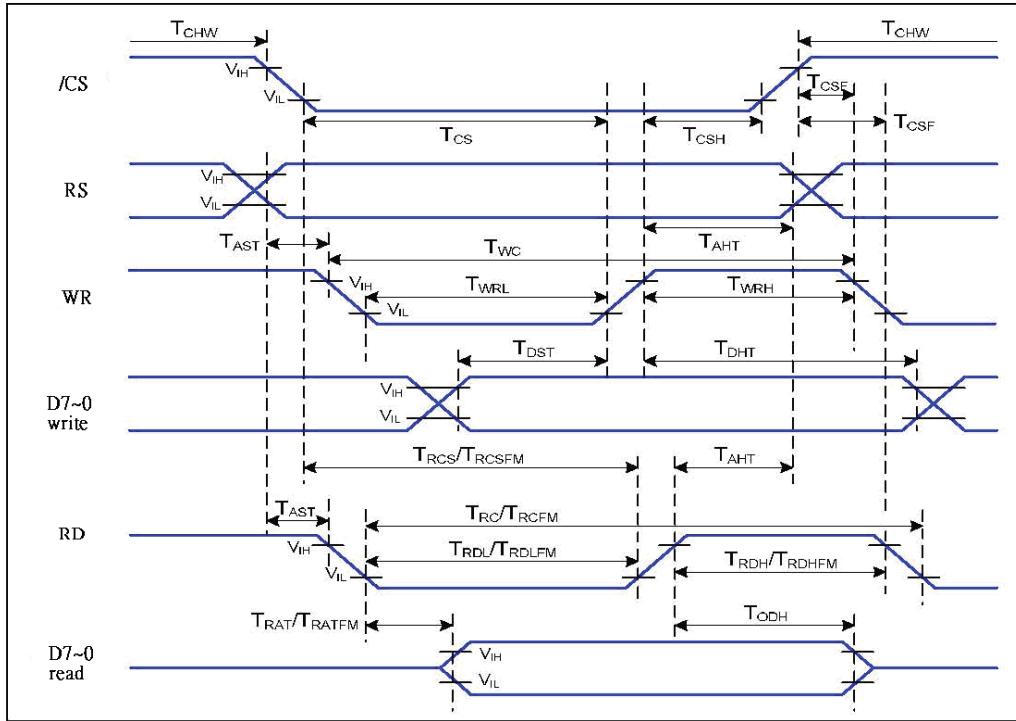
Pin NO	Symbol	Function
1,20	NC	No Connection
2	LEDK	Negative Power Supply for LED
3	LEDA	Positive Power Supply for LED
4	VDDI	Power supply for I/O system
5	/CS	Chip select for booster circuit
6~13	D7~0	8-Bit data bus,8080series
14	RD	Read Enable input pin
15	WR	Write enable input pin
16	RS	Data/Command select input pin
17	RST	Reset control pin
18	VSS	Ground
19	VDD	Power supply for LCD

Note: VDDI is I/O system power source,the reference range is 1.65 to 3.7V,and it's usually connected with VDD for power supply.

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## 2.3 Timing Characteristics

Parallel interface characteristics: 8-bit bus (8080 series MCU interface)



Parallel interface timing characteristics (8080 series MCU interface)

T<sub>a</sub>=25 °C, V<sub>DDI</sub>=1.65~3.7V, V<sub>DD</sub>=2.7~3.3V

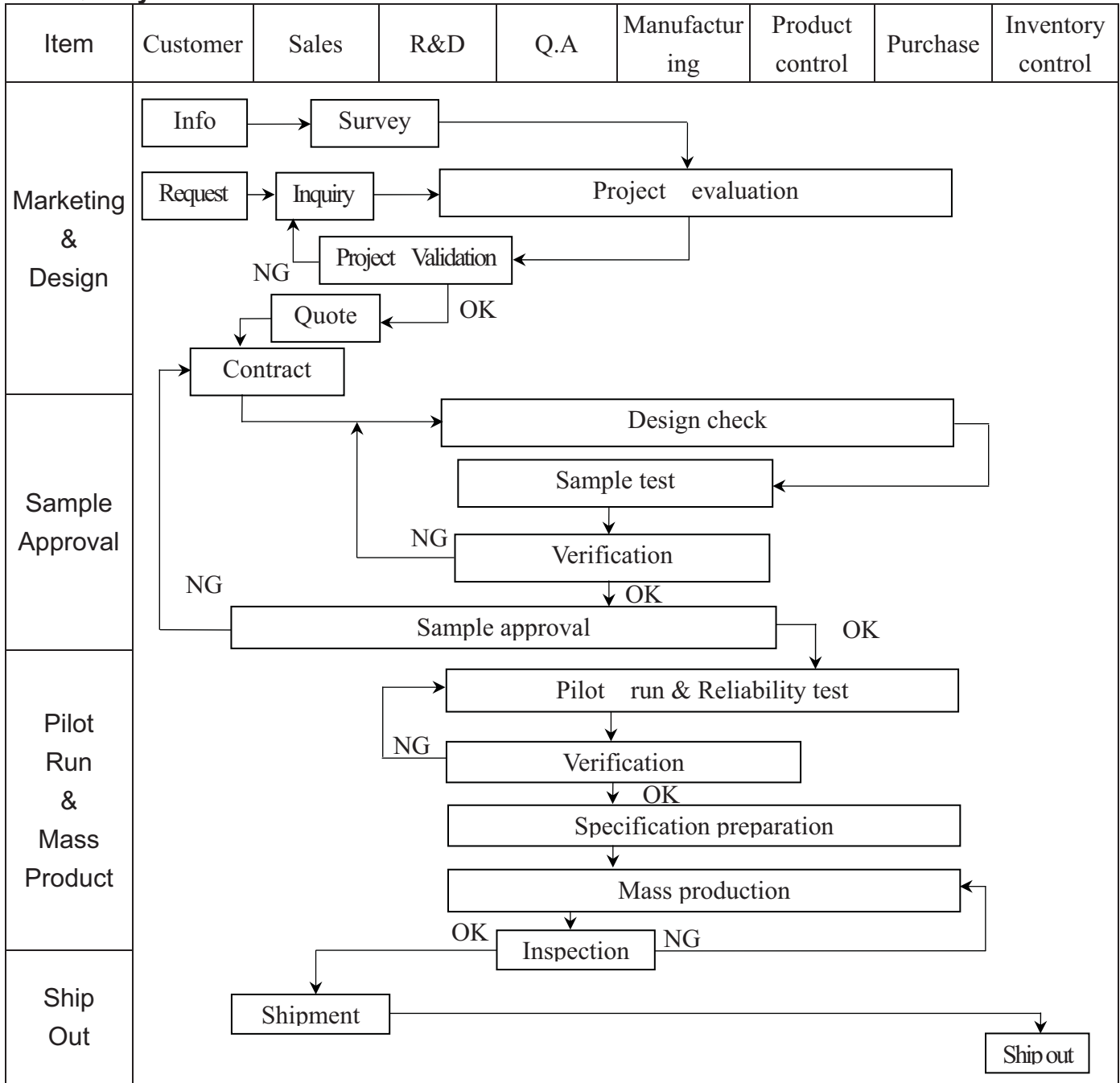
Signal	Symbol	Parameter	Min	Max	Unit	Description
RS	T <sub>AST</sub>	Address setup time	0		ns	-
	T <sub>AHT</sub>	Address hold time (Write/Read)	10		ns	
/CS	T <sub>CHW</sub>	Chip select "H" pulse width	0		ns	-
	T <sub>CS</sub>	Chip select setup time (Write)	15		ns	
	T <sub>RC</sub>	Chip select setup time (Read ID)	45		ns	
	T <sub>RCFSM</sub>	Chip select setup time (Read FM)	355		ns	
	T <sub>CSF</sub>	Chip select wait time (Write/Read)	10		ns	
	T <sub>CSH</sub>	Chip select hold time	10		ns	
WR	T <sub>WC</sub>	Write cycle	66		ns	
	T <sub>WRH</sub>	Control pulse "H" duration	15		ns	
	T <sub>WRL</sub>	Control pulse "L" duration	15		ns	
RD[ID]	T <sub>RC</sub>	Read cycle (ID)	160		ns	When read ID data
	T <sub>RDH</sub>	Control pulse "H" duration (ID)	90		ns	
	T <sub>RDLM</sub>	Control pulse "L" duration (ID)	45		ns	
RD[FM]	T <sub>RCFM</sub>	Read cycle (FM)	450		ns	When read from frame memory
	T <sub>RDHFM</sub>	Control pulse "H" duration (FM)	90		ns	
	T <sub>RDLMFM</sub>	Control pulse "L" duration (FM)	355		ns	
D7~0	T <sub>DST</sub>	Data setup time	10		ns	For CL=30pF
	T <sub>DHT</sub>	Data hold time	10		ns	
	T <sub>RAT</sub>	Read access time (ID)		40	ns	
	T <sub>RATFM</sub>	Read access time (FM)		340	ns	
	T <sub>ODH</sub>	Output disable time	20	80	ns	

8080 parallel Interface Characteristics

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### 3. QUALITY ASSURANCE SYSTEM

#### 3.1 Quality Assurance Flow Chart



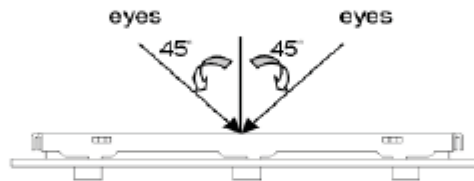
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Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service	<pre> graph TD     Info[Info] --&gt; Claim[Claim]     Claim --&gt; Failure[Failure analysis]     Failure --&gt; Report[Analysis report]     Failure --&gt; Action[Corrective action]     Action --&gt; Tracking[Tracking]         </pre>							
Q.A Activity	1. ISO 9001 Maintenance Activities 3. Equipment calibration 5. Standardization Management				2. Process improvement proposal 4. Education And Training Activities			

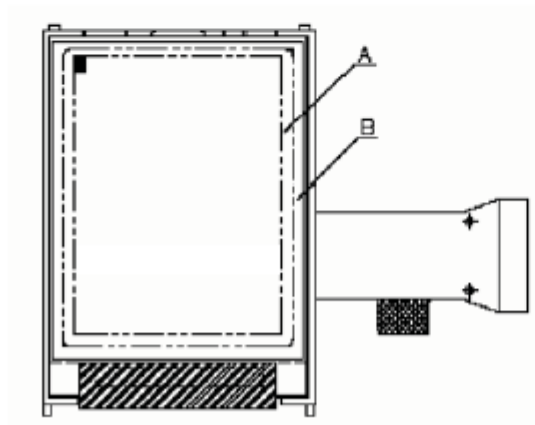
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### 3.2. Inspection Specification

- ◆ **Scope** : The document shall be applied to TFT-LCD Module for less than 3.5" (Ver.B01).
- ◆ **Inspection Standard** : MIL-STD-105E Table Normal Inspection Single Sampling Level II.
- ◆ **Equipment** : Gauge 、 MIL-STD 、 Sample
- ◆ **Defect Level** : Major Defect AQL : 0.4 ; Minor Defect AQL : 1.5
- ◆ **OUT Going Defect Level** : Sampling.
- ◆ **Standard of the product appearance test** :
  - a. **Manner of appearance test** :
    - (1). The test best be under 20W×2 fluorescent light , and distance of view must be at 30 cm.
    - (2). The test direction is base on about around 45° of vertical line.



(3). **Definition of area.**



*A* area : viewing area

*B* area : Outside of viewing area

(4). **Standard of inspection** : (Unit : mm)

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◆ Specification For TFT-LCD Module Less Than 3.5" :

(Ver.B01)

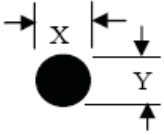
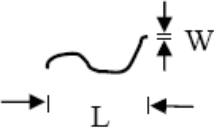
NO	Item	Criterion	Level												
01	Product condition	1. 1 The part number is inconsistent with work order of production.	Major												
		1. 2 Mixed product types.	Major												
		1. 3 Assembled in inverse direction.	Major												
02	Quantity	2. 1 The quantity is inconsistent with work order of production.	Major												
03	Outline dimension	3. 1 Product dimension and structure must conform to structure diagram.	Major												
04	Electrical Testing	4. 1 Missing line character and icon.	Major												
		4. 2 No function or no display.	Major												
		4. 3 Display malfunction.	Major												
		4. 4 LCD viewing angle defect.	Major												
		4. 5 Current consumption exceeds product specifications.	Major												
05	<b>Dot defect</b>  (Bright dot 、 Dark dot)  On -display	<table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th></th> <th>Item</th> <th>Acceptance (Q'ty)</th> </tr> </thead> <tbody> <tr> <td rowspan="4" style="text-align: center; vertical-align: middle;"><b>Dot Defect</b></td> <td>Bright Dot</td> <td style="text-align: center;"><math>\leq 2</math></td> </tr> <tr> <td>Dark Dot</td> <td style="text-align: center;"><math>\leq 3</math></td> </tr> <tr> <td>Joint Dot</td> <td style="text-align: center;"><math>\leq 2</math></td> </tr> <tr> <td>Total</td> <td style="text-align: center;"><math>\leq 3</math></td> </tr> </tbody> </table>		Item	Acceptance (Q'ty)	<b>Dot Defect</b>	Bright Dot	$\leq 2$	Dark Dot	$\leq 3$	Joint Dot	$\leq 2$	Total	$\leq 3$	Minor
			Item	Acceptance (Q'ty)											
<b>Dot Defect</b>	Bright Dot	$\leq 2$													
	Dark Dot	$\leq 3$													
	Joint Dot	$\leq 2$													
	Total	$\leq 3$													
5. 1 Inspection pattern : full white , full black , Red , Green and blue screens. 5. 2 It is defined as dot defect if defect area $> 1/2$ dot. 5. 3 The distance between two dot defect $\geq 5$ mm.															

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◆ Specification For TFT-LCD Module Less Than 3.5" :

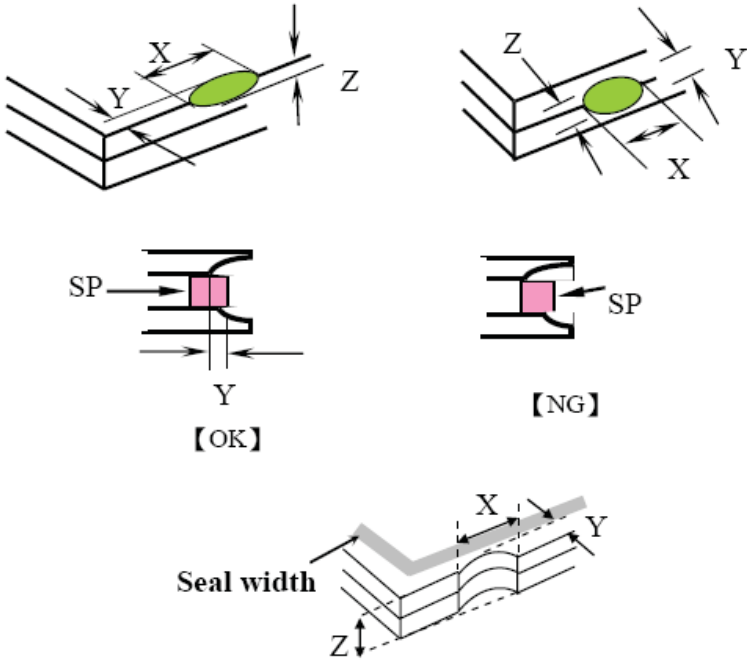
(Ver.B01)

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NO	Item	Criterion	Level																																										
06	<p>Black or white dot、scratch、contamination</p> <p>Round type</p>  <p><math>\Phi = (x + y) / 2</math></p> <p>Line type</p> 	<p>6.1 Round type ( Non-display or display ) :</p> <table border="1" data-bbox="570 369 1333 821"> <thead> <tr> <th rowspan="2">Dimension (diameter : <math>\Phi</math>)</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.15</math></td> <td colspan="2">Ignore</td> </tr> <tr> <td><math>0.15 &lt; \Phi \leq 0.20</math></td> <td colspan="2">2</td> </tr> <tr> <td><math>0.20 &lt; \Phi \leq 0.30</math></td> <td colspan="2">2</td> </tr> <tr> <td><math>\Phi &gt; 0.30</math></td> <td colspan="2">0</td> </tr> <tr> <td><b>Total</b></td> <td colspan="2">3</td> </tr> </tbody> </table> <p>6.2 Line type( Non-display or display ) :</p> <table border="1" data-bbox="548 936 1349 1346"> <thead> <tr> <th colspan="2">Dimension</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>Length (L)</th> <th>Width (W)</th> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td>---</td> <td><math>W \leq 0.03</math></td> <td>Ignore</td> <td rowspan="3">Ignore</td> </tr> <tr> <td><math>L \leq 5.0</math></td> <td><math>0.03 &lt; W \leq 0.05</math></td> <td>3</td> </tr> <tr> <td>---</td> <td><math>W &gt; 0.05</math></td> <td>As round type</td> </tr> <tr> <td colspan="2"><b>Total</b></td> <td colspan="2">3</td> </tr> </tbody> </table>	Dimension (diameter : $\Phi$ )	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.15$	Ignore		$0.15 < \Phi \leq 0.20$	2		$0.20 < \Phi \leq 0.30$	2		$\Phi > 0.30$	0		<b>Total</b>	3		Dimension		Acceptance (Q'ty)		Length (L)	Width (W)	A area	B area	---	$W \leq 0.03$	Ignore	Ignore	$L \leq 5.0$	$0.03 < W \leq 0.05$	3	---	$W > 0.05$	As round type	<b>Total</b>		3		Minor
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07	Polarizer Bubble	<table border="1" data-bbox="558 1398 1341 1801"> <thead> <tr> <th rowspan="2">Dimension (diameter : <math>\Phi</math>)</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.20</math></td> <td colspan="2">Ignore</td> </tr> <tr> <td><math>0.20 &lt; \Phi \leq 0.50</math></td> <td colspan="2">3</td> </tr> <tr> <td><math>\Phi &gt; 0.50</math></td> <td colspan="2">0</td> </tr> <tr> <td><b>Total</b></td> <td colspan="2">3</td> </tr> </tbody> </table>	Dimension (diameter : $\Phi$ )	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.20$	Ignore		$0.20 < \Phi \leq 0.50$	3		$\Phi > 0.50$	0		<b>Total</b>	3		Minor																									
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(Ver.B01)

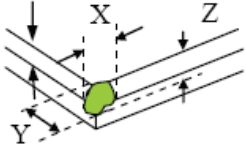
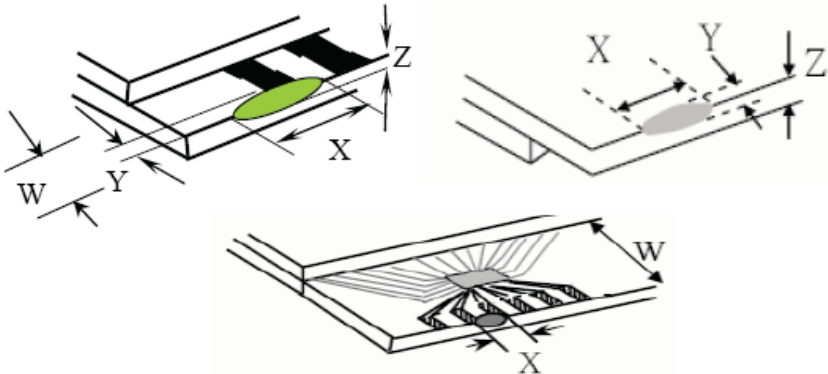
NO	Item	Criterion	Level						
08	The crack of glass	<p>Symbols :</p> <p>X : The length of crack                      Y : The width of crack.            Z : The thickness of crack                W : terminal length            t : The thickness of glass                 a : LCD side length</p>	Minor						
		<p>8.1 General glass chip :</p> <p>8.1.1 Chip on panel surface and crack between panels:</p>  <table border="1" data-bbox="565 1388 1357 1671"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td><math>\leq a</math></td> <td>Crack can't enter viewing area</td> <td><math>\leq 1/2 t</math></td> </tr> <tr> <td><math>\leq a</math></td> <td>Crack can't exceed the half of SP width.</td> <td><math>1/2 t &lt; Z \leq 2 t</math></td> </tr> </tbody> </table>		X	Y	Z	$\leq a$	Crack can't enter viewing area	$\leq 1/2 t$
X	Y	Z							
$\leq a$	Crack can't enter viewing area	$\leq 1/2 t$							
$\leq a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$							

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(Ver.B01)

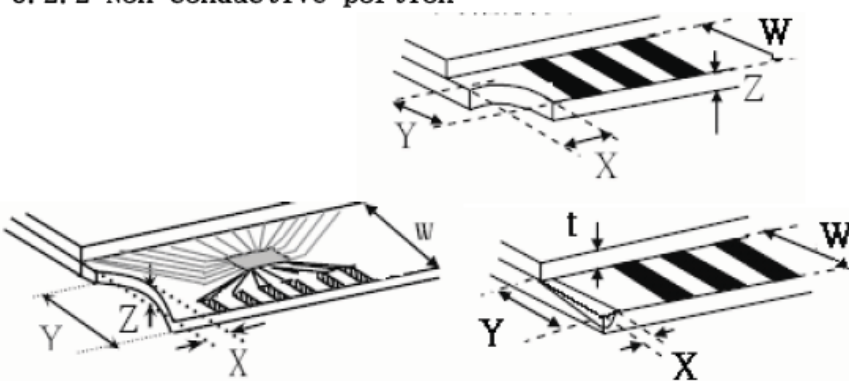
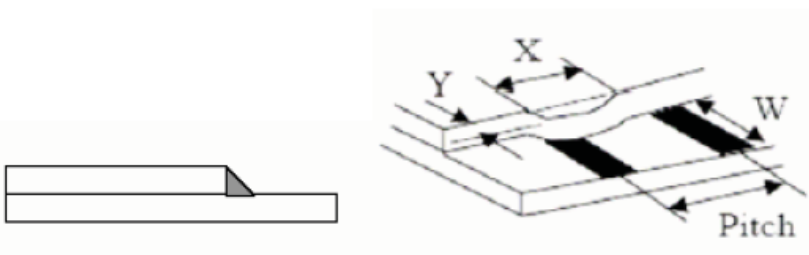
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		<p>8.1.2 Corner crack :</p>  <table border="1" data-bbox="548 720 1344 1003"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td><math>\leq 1/5 a</math></td> <td>Crack can't enter viewing area</td> <td><math>Z \leq 1/2 t</math></td> </tr> <tr> <td><math>\leq 1/5 a</math></td> <td>Crack can't exceed the half of SP width.</td> <td><math>1/2 t &lt; Z \leq 2 t</math></td> </tr> </tbody> </table>		X	Y	Z	$\leq 1/5 a$	Crack can't enter viewing area	$Z \leq 1/2 t$	$\leq 1/5 a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$
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$\leq 1/5 a$	Crack can't enter viewing area	$Z \leq 1/2 t$										
$\leq 1/5 a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$										
<p>8.2 Protrusion over terminal :</p> <p>8.2.1 Chip on electrode pad :</p>  <table border="1" data-bbox="586 1602 1352 1770"> <thead> <tr> <th></th> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>Front</td> <td><math>\leq a</math></td> <td><math>\leq 1/2 W</math></td> <td><math>\leq t</math></td> </tr> <tr> <td>Back</td> <td><math>\leq a</math></td> <td><math>\leq W</math></td> <td><math>\leq 1/2 t</math></td> </tr> </tbody> </table>		X	Y	Z	Front	$\leq a$	$\leq 1/2 W$	$\leq t$	Back	$\leq a$	$\leq W$	$\leq 1/2 t$
	X	Y	Z									
Front	$\leq a$	$\leq 1/2 W$	$\leq t$									
Back	$\leq a$	$\leq W$	$\leq 1/2 t$									

◆ Specification For TFT-LCD Module Less Than 3.5" :

(Ver.B01)

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NO	Item	Criterion	Level												
08	The crack of glass	<p><b>Symbols :</b></p> <p><b>X :</b> The length of crack                      <b>Y :</b> The width of crack.  <b>Z :</b> The thickness of crack                  <b>W :</b> terminal length  <b>t :</b> The thickness of glass                  <b>a :</b> LCD side length</p> <hr/> <p><b>8.2.2 Non-conductive portion :</b></p>  <table border="1" data-bbox="646 898 1263 1050"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td><math>\leq 1/3 a</math></td> <td><math>\leq W</math></td> <td><math>\leq t</math></td> </tr> </tbody> </table> <p>⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.</p> <p><b>8.2.3 Glass remain :</b></p>  <table border="1" data-bbox="568 1690 1247 1806"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td><math>\leq a</math></td> <td><math>\leq 1/3 W</math></td> <td><math>\leq t</math></td> </tr> </tbody> </table>	X	Y	Z	$\leq 1/3 a$	$\leq W$	$\leq t$	X	Y	Z	$\leq a$	$\leq 1/3 W$	$\leq t$	Minor
X	Y	Z													
$\leq 1/3 a$	$\leq W$	$\leq t$													
X	Y	Z													
$\leq a$	$\leq 1/3 W$	$\leq t$													

**◆ Specification For TFT-LCD Module Less Than 3.5" :**

(Ver.B01)

NO	Item	Criterion	Level
09	Backlight elements	9.1 Backlight can't work normally.	Major
		9.2 Backlight doesn't light or color is wrong.	Major
		9.3 Illumination source flickers when lit.	Major
10	General appearance	10.1 Pin type 、 quantity 、 dimension must match type in structure diagram.	Major
		10.2 No short circuits in components on PCB or FPC .	Major
		10.3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major
		10.4 Product packaging must the same as specified on packaging specification sheet.	Minor
		10.5 The folding and peeled off in polarizer are not acceptable.	Minor
		10.6 The PCB or FPC between B/L assembled distance(PCB or FPC ) is $\leq 1.5$ mm.	Minor

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## 5. PRECAUTION RELATING PRODUCT HANDLING

### 5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

### 5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully, do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is  $320 \pm 10^{\circ}\text{C}$  and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM

### 5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is  $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$  and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

### 5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period  
The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility  
This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment , fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.





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